

PRODUCT INTRODUCTION

MP1590B

Network Performance Tester

EoS Function

ANRITSU CORPORATION

Copyright © 2005 by ANRITSU CORPORATION

The contents of this manual shall not be disclosed in any way or reproduced in any media without the express written permission of Anritsu Corporation.

MP1590B Network Performance Tester

EoS Function–Product Introduction

Anritsu Corporation

17 May 2005

Discover What's Possible™

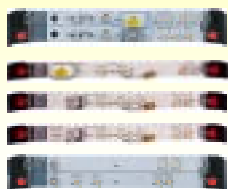
Anritsu

1 / 35

What's MP1590B?

Jitter Test

- Differential I/F
- High Precision
- 10.3G



Compliant Test

- OTN
- SONET/SDH
- PDH, DSn



NGN Test

- EoS
- VCAT
- LCAS



Ethernet/IP Test

- 10M/100M
- Gigabit
- 10 Gigabit



**One Box Tester
supporting
Converged Network**



MP1590B

Discover What's Possible™

Anritsu

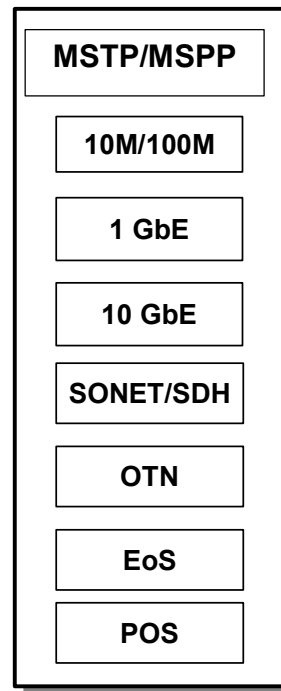
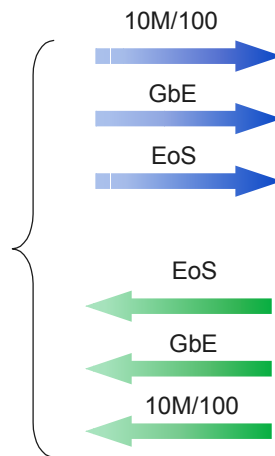
2 / 35

Applications

MSTP/MSPP Measurement



MP1590B



- Single tester support for convergence test with different I/Fs
- Independent I/F application

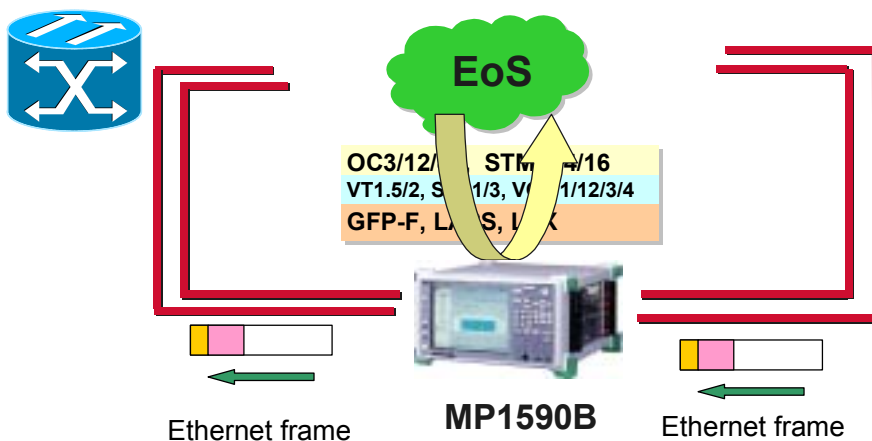
Discover What's Possible™

Anritsu

3 / 35

Applications

EoS Measurement



- Testing EoS encapsulation function
- Testing throughput for each of Ethernet to EoS, and EoS to Ethernet
- Simultaneous error/alarm measurement in Ethernet and SONET/SDH layers

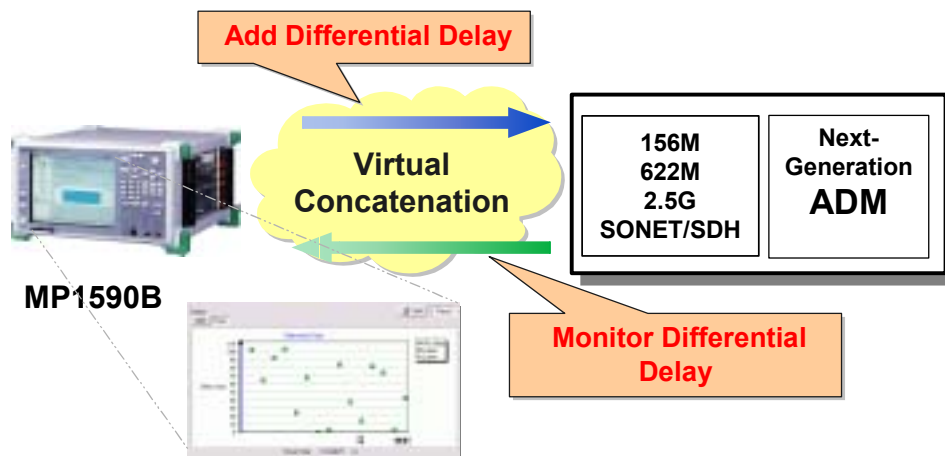
Discover What's Possible™

Anritsu

4 / 35

Applications

Differential Delay Measurement



- Differential Delay Tolerance inspection (also supports Through Mode)
- Differential Delay adds up to 512 ms to each CH of VCG independently
- Delay reproduces an actual network with the continuously Sweep mode

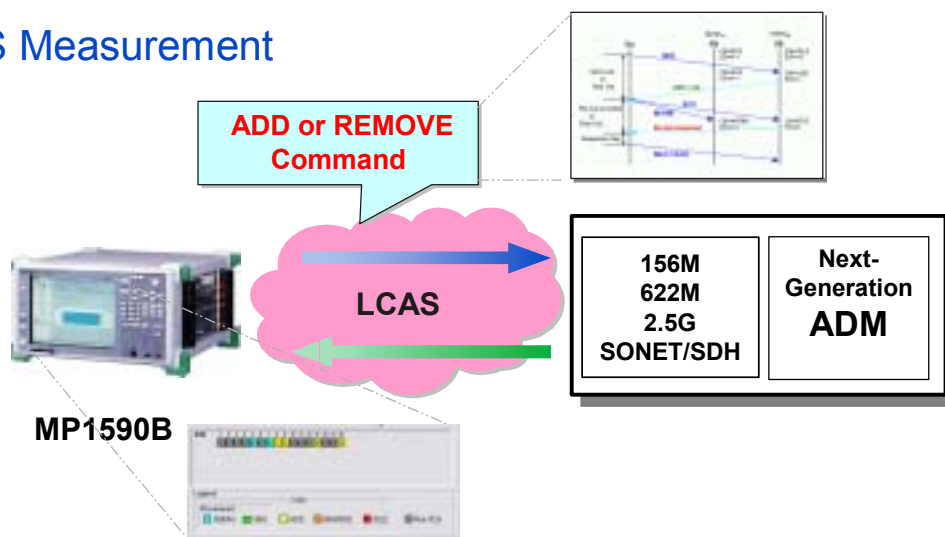
Discover What's Possible™

Anritsu

5 / 35

Applications

LCAS Measurement



- Testing bandwidth control using LCAS command during data transfer
- LCAS Sequence measurement combining multiple LCAS command
- LCAS Sequence analysis using Capture function

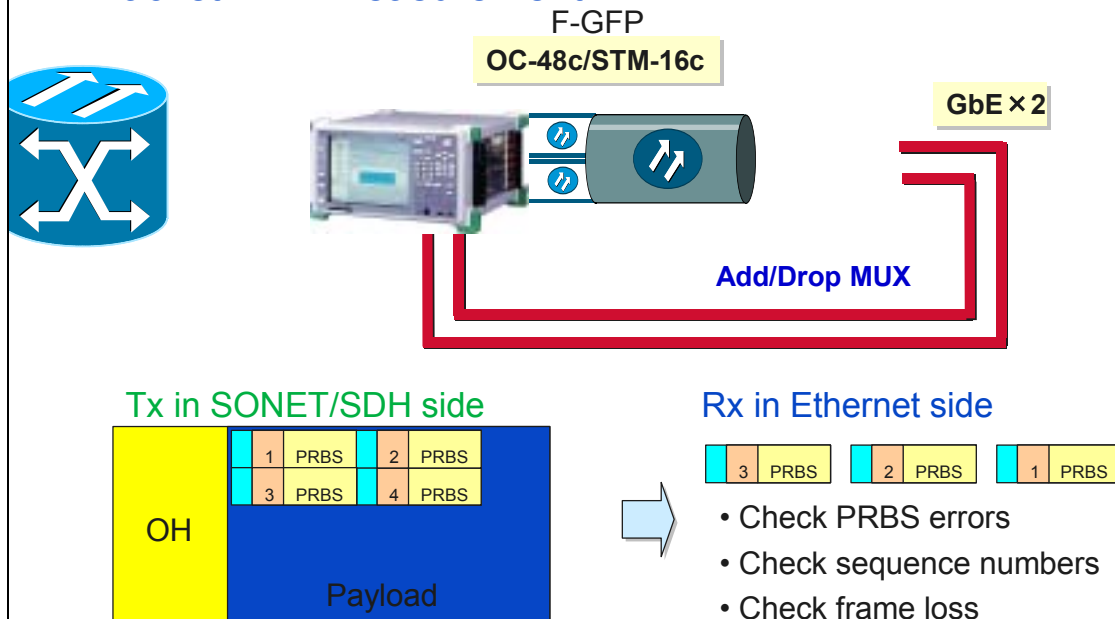
Discover What's Possible™

Anritsu

6 / 35

Applications

Packet BER Measurement



Discover What's Possible™

Anritsu

7 / 35

Key Features

- Flexible VCAT Group
- Virtual Concatenation Differential Delay measurement
- Enhanced LCAS function
- Path Monitor function
- Transmitted EoS frames
- Counter/256MB Capture
- Ethernet module for both MD1230 and MP1590
- Ethernet/IP Performance Test
- Multi Flow counter
- Ethernet Clock tolerance test
- Ethereal®

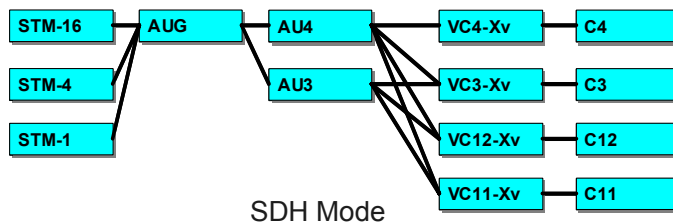
Discover What's Possible™

Anritsu

8 / 35

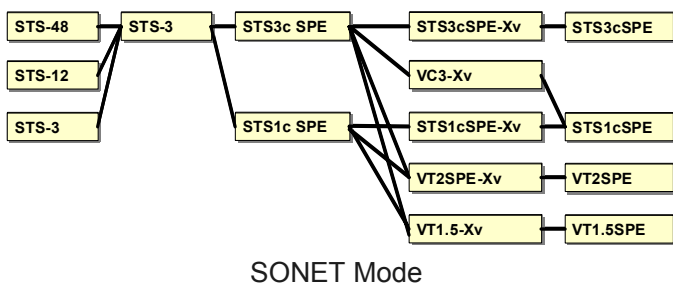
Virtual Concatenation – Mapping Structure

Virtual concatenation



Path	VCG Size
STS-3c/VC	1 to 16
STS-1/VC3(AU)	1 to 48
VT2/VC12	1 to 63
VT1.5/VC11	1 to 64

Mapping for both High order and Low order
VCG Size conforms to ITU-T recommendations
Without AUG boundary in LO



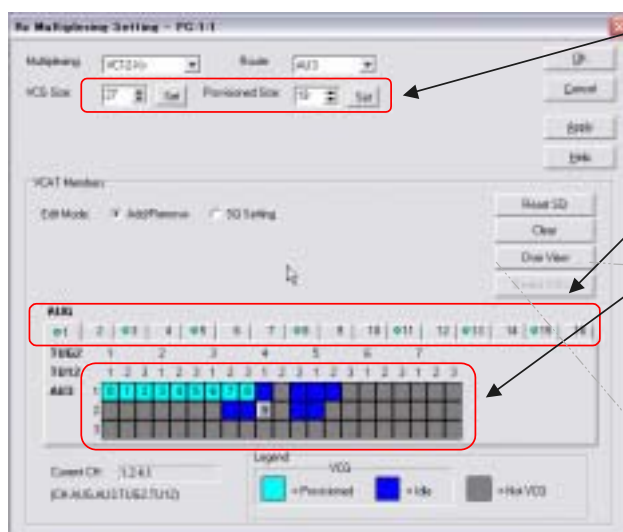
Discover What's Possible™

Anritsu

9 / 35

Virtual Concatenation – VCAT Member Configuration

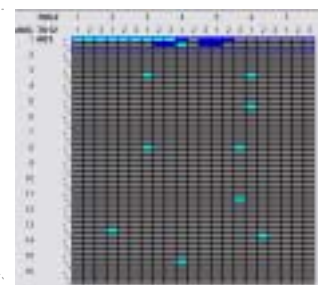
Virtual Concatenation Group Member Setting



Independent setting for VCG Size and Provisioned Size

VCG(Provisioned and Idle ch) without AUG boundary
AUG/STS3c

One click member selection



Virtual Concatenation Group setting

VCG member Overview Screen

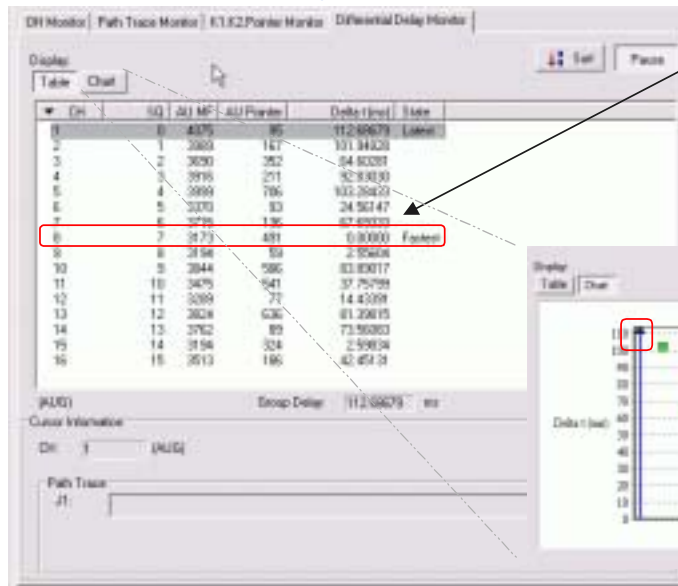
Discover What's Possible™

Anritsu

10 / 35

Virtual Concatenation – Differential Delay monitor

Differential Delay monitor



Differential Delay monitor screen (Table View)

Measure Delay of each Channel for VCG
(The earliest Channel is datum.)
When Chart View is used, the correlation between each channel can be easily understood.



Chart View

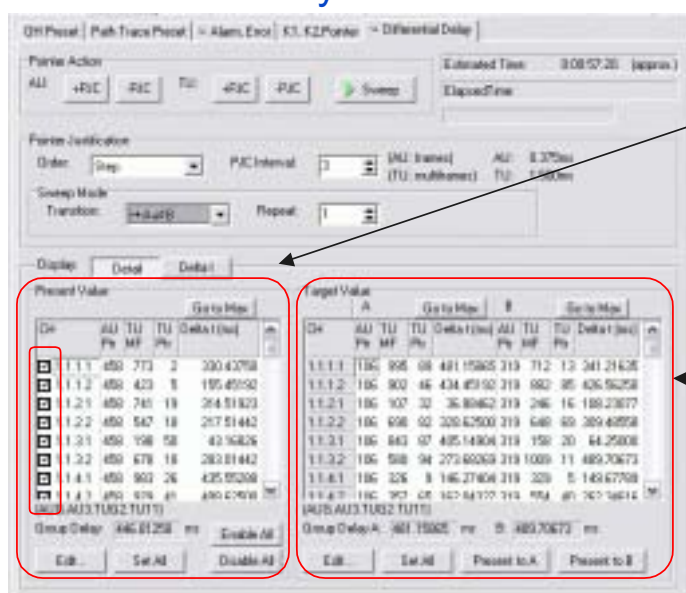
Discover What's Possible™

Anritsu

11 / 35

Virtual Concatenation – Deferential Delay Generator

Differential Delay Generator



Difference Delay Tx setting

Displays current Tx Differential Delay. The Delay value can be changed directly here.

When using the Sweep mode, set the Target value to be changed here. Two Target values can be set; it is also possible to set the coming or going, etc., for each value.

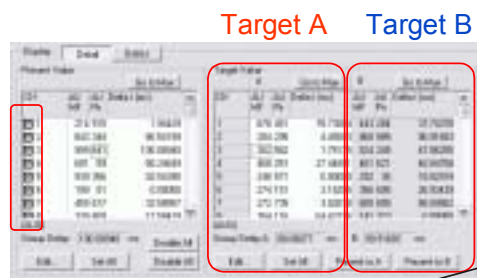
Discover What's Possible™

Anritsu

12 / 35

Virtual Concatenation – Differential Delay Generator

Differential Delay Generator – Sweep Mode



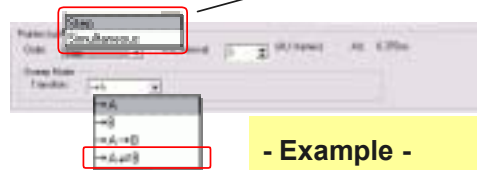
The Delay value can be set independently for each Channel. In the Sweep Mode, two values can be set as the Target value.

This sets the Delay order.

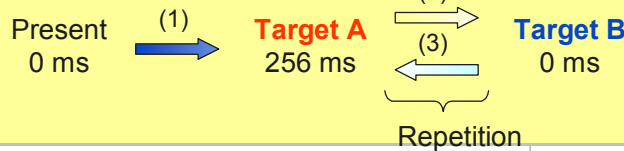
Step: Changes each Channel in turn

Simultaneous: Changes each Channel simultaneously

Measurement can be performed under Delay conditions approximating those of an actual network by changing the 2 points.



- Example -



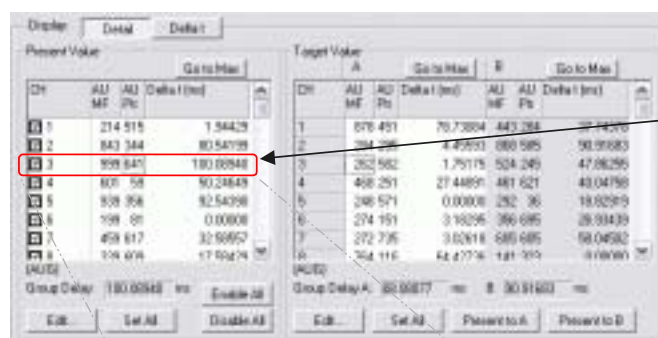
Discover What's Possible™

Anritsu

13 / 35

Virtual Concatenation – Deferential Delay Generator

Differential Delay Generator – Direct mode



When the Delay value (not Sweep value) is inserted directly, the Present Value is changed.



The Delay value can be input in each of the

- MFI value
- Pointer value
- Delta value

formats.

VCG member Overview Screen

Discover What's Possible™

Anritsu

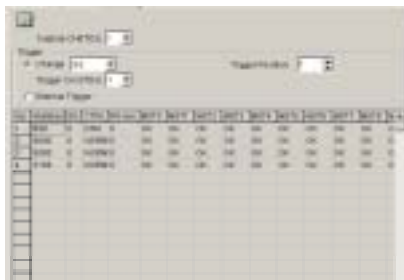
14 / 35

LCAS – Monitor/Capture



LCAS Monitor

By using the LCAS monitor function, all members of the VC group and all the MST (Member Status) can be monitored simultaneously to confirm the current status.



LCAS Capture

Up to 64 LCAS sequences can be captured and the trigger and capture target members can be selected freely. Since the time required for each sequence/command can be displayed, the time required for the LCAS sequence can be measured and failure analysis can be performed.

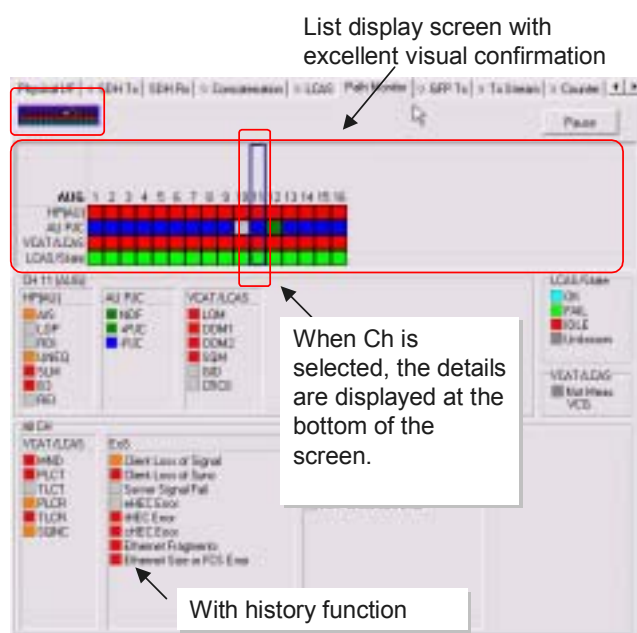
Discover What's Possible™

Anritsu

17 / 35

VCAT/LCAS – Path Monitor

Path Monitor Screen



SONET/SDH Error, Alarm
RJC

VCAT Error, Alarm

LCAS Error, Alarm

EoS Error

PPP/IP/TCP/UDP Error

of each VCG channel is measured in real time. Since all VCG Provisioned Channel operation can be measured simultaneously, more detailed measurement is possible.

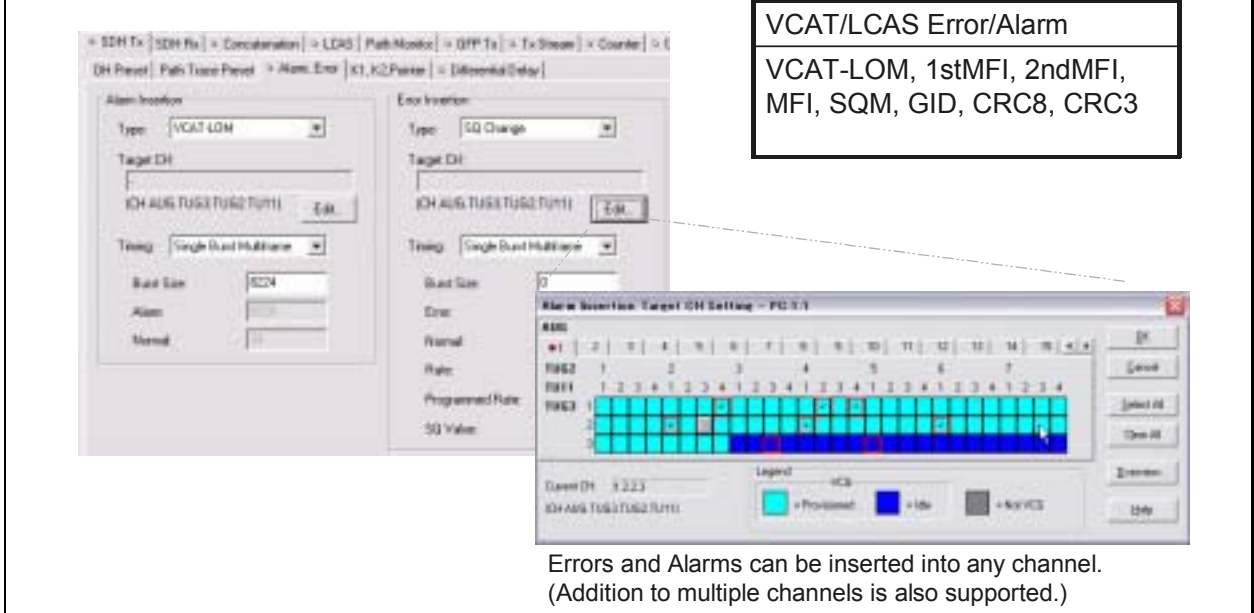
Discover What's Possible™

Anritsu

18 / 35

VCAT/LCAS – Error/Alarm

Error/Alarm Addition Screen



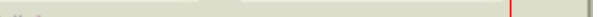
Discover What's Possible™  19 / 35

Anritsu

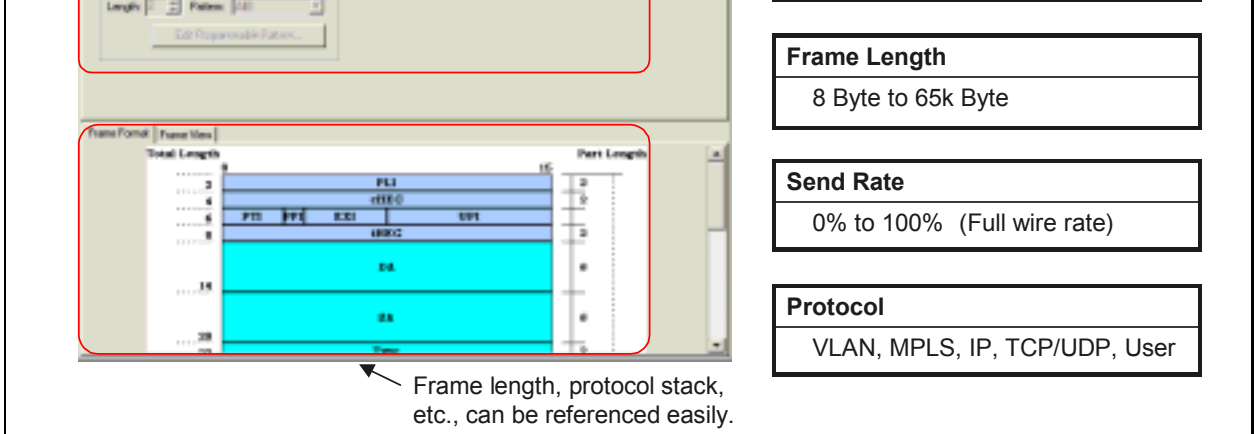
EoS – Transmitting


F-GFP Frame Editing Screen

The GFP header value can be edited freely.



Mapping
Framemapped-GFP, PPP, Cisco HDLC, MAPOS, LEX, LAPS(X.86), Bulk, Unframe



Discover What's Possible™  20 / 35

Anritsu

10

EoS – Real Time Counters

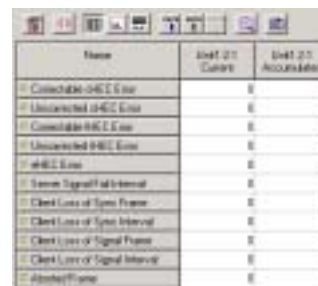
GFP Counters

- Transmitted GFP Frame
- Transmitted GFP Byte
- Received GFP Frame
- Received GFP Byte
- GFP FCS Error
- GFP uncorrectable cHEC
- GFP correctable cHEC
- GFP uncorrectable tHEC
- GFP correctable tHEC
- GFP eHEC Error
- Client Loss of Signal Frame
- Client Loss of Signal Interval

- Client Loss of Sync Frame
- Client Loss of Sync Interval
- Server Signal Fail Interval

➤ More than 40 SONET/SDH, VCAT, LCAS Counters

More than 60 IP/Ethernet Counters



Name	Unit	Unit 21	Unit 22
Correctable cHEC Error		0	0
Uncorrectable cHEC Error		0	0
Correctable tHEC Error		0	0
Uncorrectable tHEC Error		0	0
eHEC Error		0	0
Server Signal Fail Interval		0	0
Client Loss of Sync Frame		0	0
Client Loss of Sync Interval		0	0
Client Loss of Signal Frame		0	0
Client Loss of Signal Interval		0	0
Altered Frame		0	0

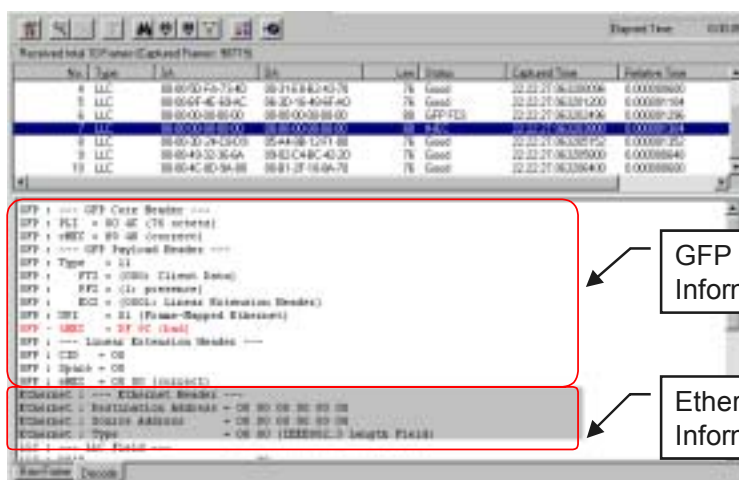
Discover What's Possible™

Anritsu

21 / 35

EoS – Capture and Decode

EoS Frame Capture and Decode



The screenshot shows a table of captured frames with columns: No., Type, Src, Dst, Len, Status, Captured Time, and Relative Time. Below the table, a detailed decode view is shown for a selected frame, highlighting the GFP Header Information and Ethernet Header Information.

EoS Frame Capture

256 MB

GFP Header Information

Ethernet Header Information

The EoS receive frame can be stored and protocol analysis performed for GFP, PPP, Ethernet, TCP/UDP, etc. This permits effective troubleshooting when used in combination with the trigger and filter functions.

Discover What's Possible™

Anritsu

22 / 35

Ethernet Module Lineup

Standard Modules

MU120101A 10M/100M Ethernet Module

MU120102A Gigabit Ethernet Module

Advanced Protocol Modules

MU120111A 10/100M Ethernet Module

MU120112A Gigabit Ethernet Module

★
Update MU120118B/C 10Gigabit Ethernet Module

Power Protocol Modules

★
New MU120121A 10/100/1000M Ethernet Module

★
New MU120122A Gigabit Ethernet Module

Supports use of MD1230 Ethernet module for making genuine Ethernet tests

Performance Tests

Throughput, Latency, Protocol Analyze

Protocol Tests

IPv6, OSPFv2/v3, BGP4+, MPLS, PIM-SMv2, MLDA, IGAP

Enhanced Tests

10M/100M/1000M I/F, Multi-flow Counter, VLAN Stacking, Clock Tolerance

Discover What's Possible™

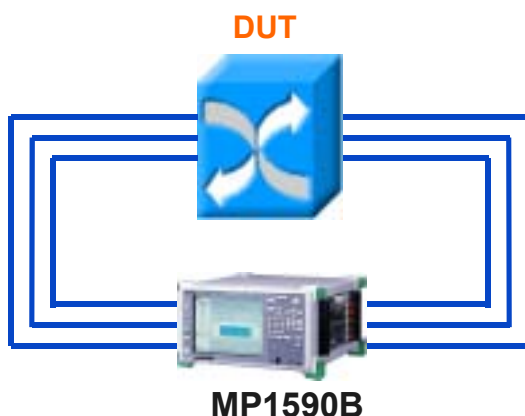
Anritsu

23 / 35

Performance Test

Performance Test

Supports Ethernet and POS performance and load tests



EoS doesn't support

- Throughput
- Latency*3
- Variable Packet Counters
- VLAN / Stacking VLAN*1
- MPLS
- IPv6*2
- Short and Jumbo Frames

10M/100M, Gigabit, 10M/100M/1000M, 10Gigabit Ethernet I/F
GFP/PPP/LEX/LAPS/Cisco HDLC/MAPOS at 156M to 2.5G

Discover What's Possible™

*1: Stacking VLAN supported by Power Protocol module
*2: EoS Unit does not support IPv6 measurement

Anritsu

24 / 35

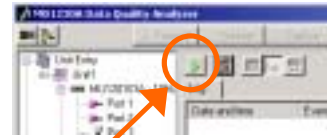
Automatic Test

Automatic Test / Ethernet module

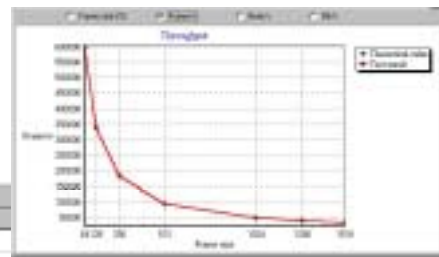
An automatic test function meeting FC2544/RFC2889 recommendations is built-in. Simply clicking the start button starts automatic measurement and displays measurement results in the recommended format.

RFC2544 Auto-test Item

- Throughput
- Latency
- Frame Loss Rate
- Back to Back Frames
- System Recovery
- Reset



One click operation



Learning Rates:	1			
Back-to-back Frames setting:				
Number of Tests:	10			
Number of Frames				
Frame size	Average	1	2	3
64	1120470	1120470	1120470	1120470
128	675670	675670	675670	675670
256	362330	362330	362330	362330
512	187972	187972	187972	187972
1024	95700	95700	95700	95700

Discover What's Possible™

This function is not supported by the EoS Unit.

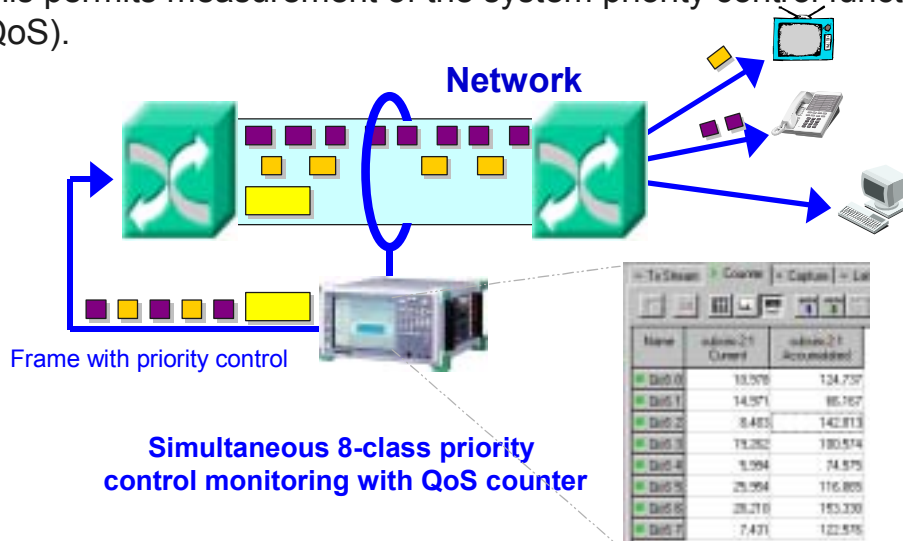
Anritsu

25 / 35

QoS Test

QoS Test

The EoS and Ethernet module have a built-in 8-class QoS counter. This permits measurement of the system priority control function (QoS).



Discover What's Possible™

Anritsu

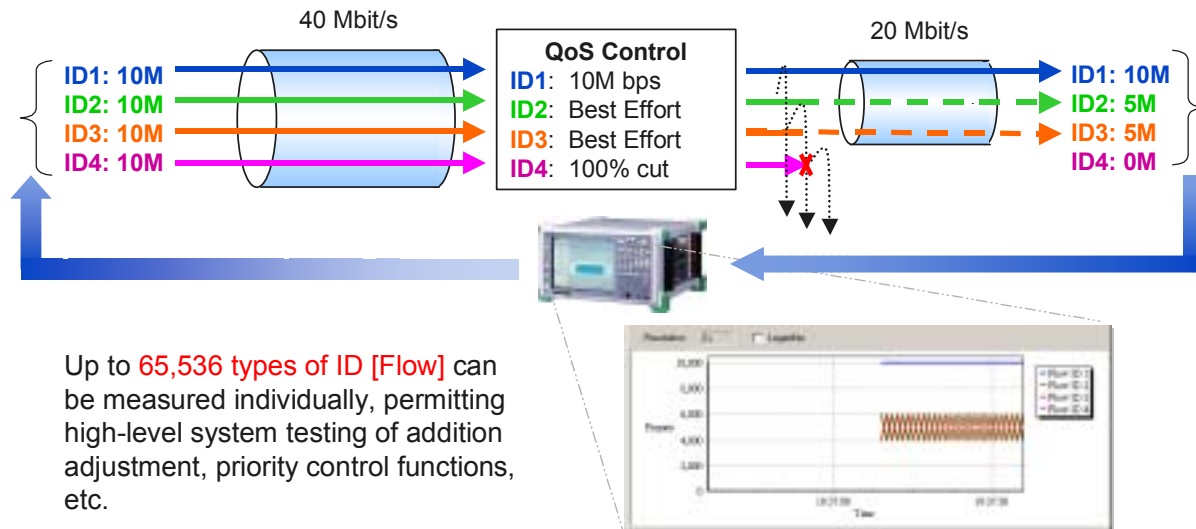
26 / 35

QoS Test – Multi-flow Counter

Multi-flow Counter

*Supported by Power Protocol module

In more complex QoS testing, data for each individual ID [Flow] can be measured more effectively using the Multi-flow Counter function.



Up to 65,536 types of ID [Flow] can be measured individually, permitting high-level system testing of addition adjustment, priority control functions, etc.

Discover What's Possible™

Anritsu

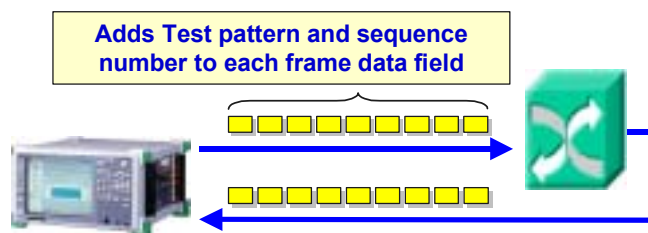
27 / 35

Ethernet BER Test

Packet BER Test (Option-11)

Measures sequence No. and BER of packet data field

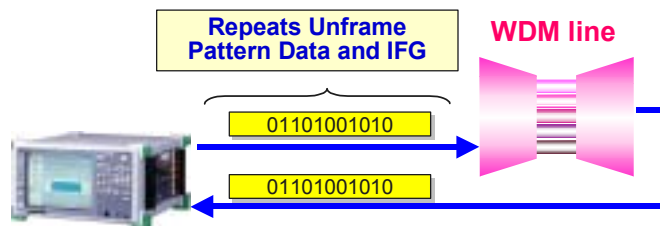
- Packet BER
- Sequence check
- Packet Loss



Unframed BER Test

Measures BER in all fields of packets (Inserts IFG between packets)

- All0, All1
- User 16bits
- PRBS23
- PRBS31
- CRPAT
- CJPAT



Discover What's Possible™

Anritsu

28 / 35

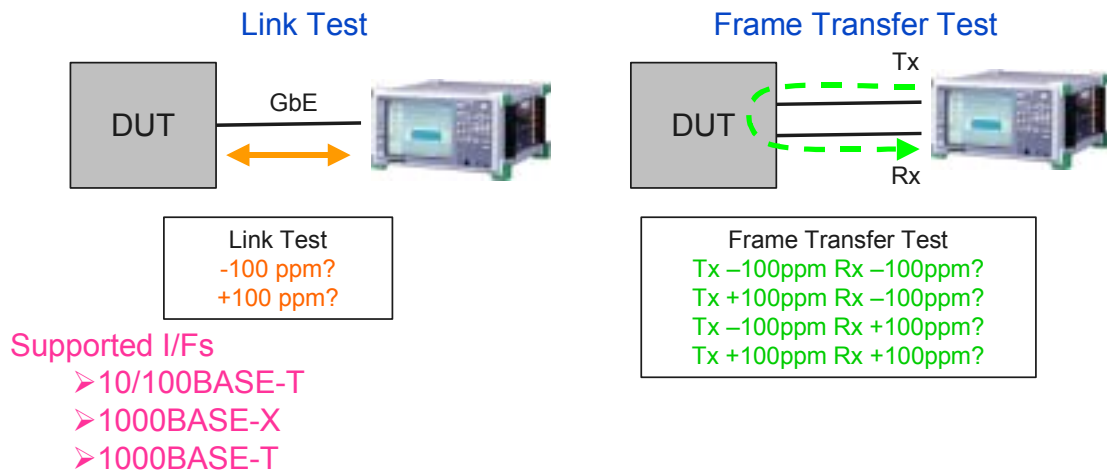
Ethernet Clock Tolerance Test

Variable Send Clock Function

*Supported by Power Protocol module

The Power Protocol module has a built-in function for varying the send clock. By using this function, the DUT clock tolerance can be tested.

Setting range: **-100 ppm to +100 ppm** (Clock accuracy: -4 ppm to +4 ppm)



Discover What's Possible™

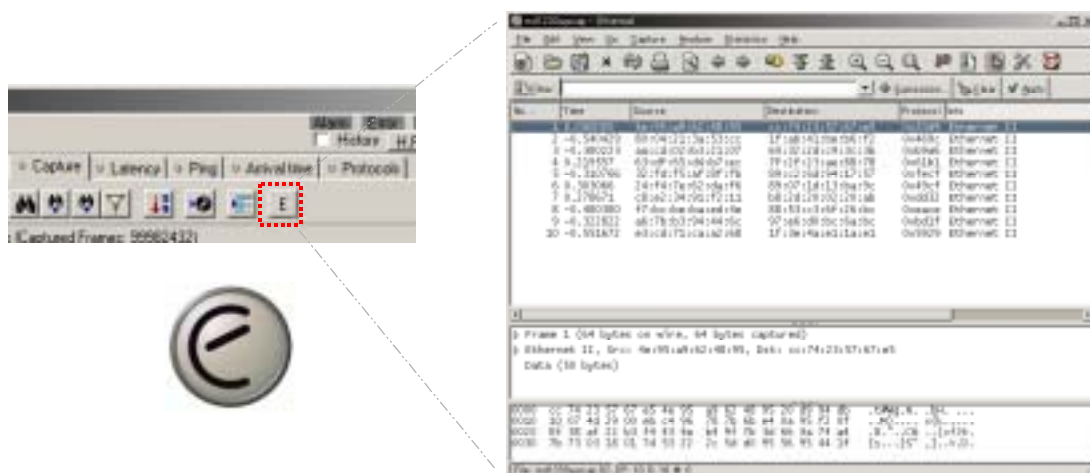
Anritsu

29 / 35

Protocol Analysis

Ethereal®

In addition to the standard built-in analysis functions, protocol analysis for the latest protocols is also supported using Ethereal®.



Ethereal® is a registered trademark of Ethereal Inc. in the USA.

This is free open-source software that may be installed by the customer at his or her own risk.

Discover What's Possible™

Anritsu

30 / 35

OTN/SONET/SDH Test

OTN, SONET/SDH

- **ITU-T G.709, G.8251 Recommendations**
 - OTU2: 10.7G, OTU1: 2.66G
 - FEC: Reed-Solomon Code RS (255, 239)
- **Telcordia GR-253, ITU-T G.703, G.707 Recommendations**
 - OC 1~192/STM 0~64, DSn/PDH
- **Main Functions**
 - Overhead Tests: OTN, SONET/SDH OH setting and monitoring
 - Arbitrary concatenation
 - Error/Alarm testing and detection
 - Thru mode: Transparent, OH overwrite
 - APS (Automatic Protection Switch) testing
 - Clock/Frame sync signal output
 - External light source input

Discover What's Possible™

Anritsu

31 / 35

Jitter Test

Jitter Measurement

- **Basic measurements**
 - Jitter Transfer
 - Jitter Tolerance
 - Jitter Generation
 - Output Jitter
 - Wander Generation/Measurement (P-P, +P, -P, TIE)
- **High-accuracy Jitter Measurement and Excellent Measurement Reproducibility**
 - **MP1590B Option 30 High-accuracy Jitter Measurement**
 - Generation measurement accuracy: ± 20 mUlp-p
 - Measurement reproducibility: ± 5 mUlp-p
 - Stable measurement without optical input level and wavelength dependency

Discover What's Possible™

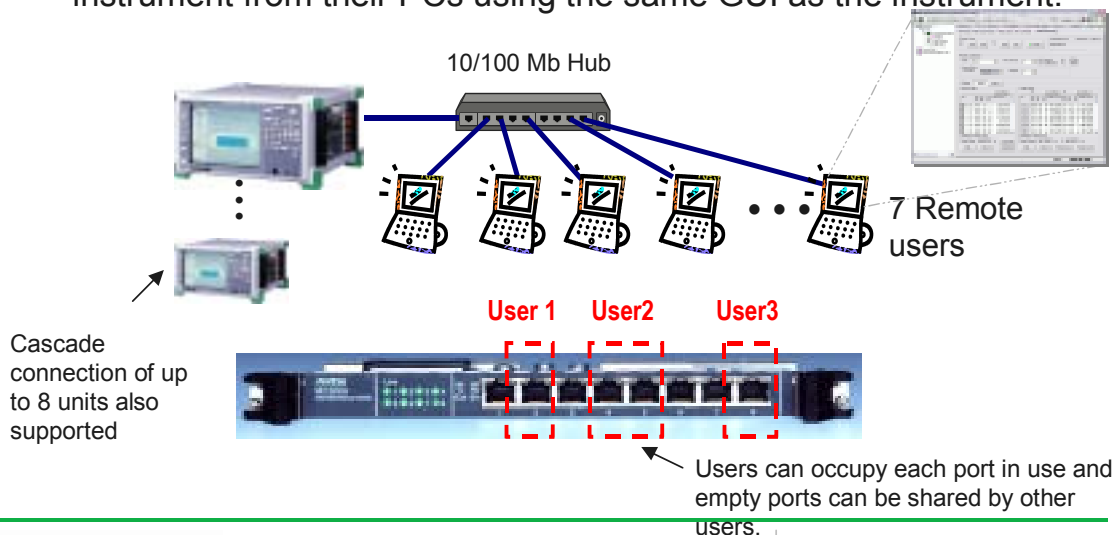
Anritsu

32 / 35

Remote Control

Remote Control

Remote control software (sold separately) is also available. This software permits up to 8 remote users to control one measuring instrument from their PCs using the same GUI as the instrument.



Discover What's Possible™

Anritsu

33 / 35

Unit Configuration

Combination Examples

MP1590B



Ethernet Only

Slot1	
Slot2	
Slot3	
Slot4	
Slot5	
Slot6	

Ethernet module can be used in slots 3, 4, 5 and 6

10/10.7G with Jitter

Slot1	MU150100A
Slot2	
Slot3	Optical Tx Unit
Slot4	Optical Rx Unit
Slot5	Jitter Unit
Slot6	

10/10.7G

Slot1	MU150100A
Slot2	
Slot3	Optical Tx Unit
Slot4	Optical Rx Unit
Slot5	
Slot6	

Ethernet module can be used in slots 5 and 6

2.5/2.6G with Jitter

Slot1	MU150101A
Slot2	
Slot3	
Slot4	
Slot5	Jitter Unit
Slot6	

Ethernet module can be used in slots 3 and 4

2.5/2.6G

Slot1	MU150101A
Slot2	
Slot3	
Slot4	
Slot5	
Slot6	

Ethernet module can be used in slots 3, 4, 5 and 6

Discover What's Possible™

Anritsu

34 / 35

Thank you very much.

Discover What's Possible™

Anritsu

35 / 35



Specifications are subject to change without notice.

ANRITSU CORPORATION

1800 Onna, Atsugi-shi, Kanagawa, 243-8555 Japan
 Phone: +81-46-223-1111
 Fax: +81-46-296-1264

● **U.S.A.****ANRITSU COMPANY****TX OFFICE SALES AND SERVICE**

1155 East Collins Blvd., Richardson, TX 75081, U.S.A.
 Toll Free: 1-800-ANRITSU (267-4878)
 Phone: +1-972-644-1777
 Fax: +1-972-644-3416

● **Canada****ANRITSU ELECTRONICS LTD.**

700 Silver Seven Road, Suite 120, Kanata,
 ON K2V 1C3, Canada
 Phone: +1-613-591-2003
 Fax: +1-613-591-1006

● **Brazil****ANRITSU ELETRÔNICA LTDA.**

Praca Amadeu Amaral, 27 - 1 andar
 01327-010 - Paraiso, Sao Paulo, Brazil
 Phone: +55-11-3283-2511
 Fax: +55-11-3886940

● **U.K.****ANRITSU LTD.**

200 Capability Green, Luton, Bedfordshire LU1 3LU, U.K.
 Phone: +44-1582-433280
 Fax: +44-1582-731303

● **Germany****ANRITSU GmbH**

Grafenberger Allee 54-56, 40237 Düsseldorf, Germany
 Phone: +49-211-96855-0
 Fax: +49-211-96855-55

● **France****ANRITSU S.A.**

9, Avenue du Québec Z.A. de Courtabœuf 91951 Les
 Ulis Cedex, France
 Phone: +33-1-60-92-15-50
 Fax: +33-1-64-46-10-65

● **Italy****ANRITSU S.p.A.**

Via Elio Vittorini, 129, 00144 Roma EUR, Italy
 Phone: +39-06-509-9711
 Fax: +39-06-502-2425

● **Sweden****ANRITSU AB**

Borgafjordsgatan 13 164 40 Kista, Sweden
 Phone: +46-853470700
 Fax: +46-853470730

● **Finland****ANRITSU AB**

Teknobulevardi 3-5, FI-01530 Vantaa, Finland
 Phone: +358-9-4355-220
 Fax: +358-9-4355-2250

● **Denmark****Anritsu AB Danmark**

Korskildelund 6 DK - 2670 Greve, Denmark
 Phone: +45-36915035
 Fax: +45-43909371

● **Singapore****ANRITSU PTE LTD.**

10, Hoe Chiang Road #07-01/02, Keppel Towers,
 Singapore 089315
 Phone: +65-6282-2400
 Fax: +65-6282-2533

● **Hong Kong****ANRITSU COMPANY LTD.**

Suite 923, 9/F., Chinachem Golden Plaza, 77 Mody
 Road, Tsimshatsui East, Kowloon, Hong Kong, China
 Phone: +852-2301-4980
 Fax: +852-2301-3545

● **P. R. China****ANRITSU COMPANY LTD.****Beijing Representative Office**

Room 1515, Beijing Fortune Building, No. 5 North
 Road, the East 3rd Ring Road, Chao-Yang District
 Beijing 100004, P. R. China
 Phone: +86-10-6590-9230

● **Korea****ANRITSU CORPORATION**

8F Hyun Juk Bldg. 832-41, Yeoksam-dong,
 Kangnam-ku, Seoul, 135-080, Korea
 Phone: +82-2-553-6603
 Fax: +82-2-553-6604

● **Australia****ANRITSU PTY LTD.**

Unit 3/170 Forster Road Mt. Waverley, Victoria, 3149,
 Australia
 Phone: +61-3-9558-8177
 Fax: +61-3-9558-8255

● **Taiwan****ANRITSU COMPANY INC.**

7F, No. 316, Sec. 1, NeiHu Rd., Taipei, Taiwan
 Phone: +886-2-8751-1816
 Fax: +886-2-8751-1817

050203



Printed on 100%
 Recycled Paper

No. MP1590B_Step2-E-I-1-(1.00) 公知 Printed in Japan 2005-7 AKD