

MP1764A
Error Detector
Option 02 Differential Data Input Function
Option 03 Clock Recovery Function
Operation Manual

First Edition

- **Read this manual before using the equipment.**
- **To ensure that the equipment is used safely, read the "For Safety" in the MP1764A Error Detector Operation Manual first.**
- **Keep this manual with the equipment.**
- **The MP1764A-02 and MP1764A-03 do not conform to CE Marking.**

ANRITSU CORPORATION

MP1764A
Error Detector Option 02 Differential Data Input Function Option 03 Clock Recovery Function
Operation Manual

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MP1764A-02 Differential Data Input and MP1764A-03 Clock Recovery Function

For an operation explanation of the MP1764A-02 Differential Data Input Function (hereafter, referred to as Option 02) and MP1764A-03 Clock Recovery Function (hereafter, referred to as Option 03), refer to part of “MP1764D Error Detector Operation Manual.” The following lists show the referred pages.

In addition, the MP1764D Error Detector Operation Manual is included in this manual. Read replacing the model name with the MP1764A.

As for operations of the MP1764A mainframe, refer to “MP1764A Error Detector Operation Manual” (Document No. W0887AE).

When option 02 is installed.

Section	Page	Supplemental explanation
1.1, 1.2 3.1	1-2, 1-7 3-2 to 3-3	When option-03 is not installed, the recovery clock due to the clock recovery function is not output.
4.2, 4-3 7.3	4-5, 4-7, 4-17, 4-18 7-4 to 7-8	This causes each operation and operation of the PLL reset key, clock select key, clock recovery frequency rotary encoder to be invalid.

When option 03 is installed.

Section	Page	Supplemental explanation
1.1, 1.2 3.1	1-2, 1-6 3-2	When option 02 is not installed, data input is not differential.
4.1 to 4.3 6.2	4-3 to 4-16 6-3	This causes each function and operation of $\overline{\text{DATA}}$ input, termination conditions select key, DISPLAY switching key, DATA/ $\overline{\text{DATA}}$ TRACKING, DIFFERENCE ADJUST, single/differential switching key to be invalid.
4.1 6.1, 6.3 7.3	4-3 6-2, 6-4 7-4	Ignore the cable connection of $\overline{\text{DATA}}$ input in the figure.
