Anritsu - GRL

PCIe 3.0 CEM Rx Test Application

Release Note
Third Edition

This software is released for PCIe CEM Rx Test.

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1. Released Software

<table>
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<tr>
<th>Edition</th>
<th>GRL CEM Rx Test Application</th>
<th>Anritsu MX190000A</th>
<th>Anritsu MX183000A</th>
<th>Scope</th>
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<tr>
<td>03</td>
<td>V1.0.22</td>
<td>V3.00.05</td>
<td>V3.06.16</td>
<td>DSAZ634A V06.20.01101</td>
</tr>
<tr>
<td>02</td>
<td>V1.0.22</td>
<td>V2.05.08</td>
<td>V3.05.00</td>
<td>DSAZ634A V06.20.01101</td>
</tr>
<tr>
<td>01</td>
<td>V1.0.11</td>
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<td>DPO73304DX 10.8.3 Build 3</td>
</tr>
</tbody>
</table>

2. Peripheral Devices

The peripheral devices of the application are shown in the table.

<table>
<thead>
<tr>
<th>Model</th>
<th>Name</th>
</tr>
</thead>
<tbody>
<tr>
<td>MP1900A</td>
<td>Signal Quality Analyzer-R</td>
</tr>
<tr>
<td>MU181000B</td>
<td>12.5GHz 4port Synthesizer (Option 02 is required.)</td>
</tr>
<tr>
<td>MU181500B</td>
<td>Jitter Modulation Source</td>
</tr>
<tr>
<td>MU195020A</td>
<td>21G/32G bit/s SI PPG</td>
</tr>
<tr>
<td>MU195040A</td>
<td>21G/32G bit/s SI ED</td>
</tr>
<tr>
<td>MU195050A</td>
<td>Noise Generator</td>
</tr>
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</table>

For the installation position of the mainframe, refer to the Anritsu website (https://www.anritsu.com).
3. Added Functions
None

4. Bug Fixes

<table>
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<tr>
<th>Edition</th>
<th>Item (Management Number)</th>
<th>Fault</th>
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<tr>
<td>01</td>
<td>Sometimes, Preset calibration fails with Tektronix scope.</td>
<td>Calibration fails when attempting to execute Preset calibration. Bug occurs in GRL software version 1.00.11 and Tektronix scope software version 10.8.3.</td>
</tr>
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</table>

5. Remaining Known Bugs
None
6. Usage Notes

The precautions for using each version are described below.

6.1 How to shorten measurement time

- BER and Margin Test

Decrease Margin Test Measurement Time and Margin Test Max Steps.

Set Retrain When Sj Frequency Changed to False.
6.2 Note on Link EQ Response time test

Perform a test with **Tx EQ Response time (Preset)** first and then perform it with **Tx EQ Response Time (Cursor)**.

When starting the test with **Tx EQ Response Time (Preset)**, the cursor values corresponding to Preset are notified from a DUT, and they are saved in MP1900A. These values are required for testing with **Tx EQ Response Time (Cursor)**.

To skip the test with **Tx EQ Response time (Preset)**:

Perform link training follow the below steps to acquire cursor values from a DUT. When replace the DUT to test **Tx EQ Response time (Cursor)**, the steps are required again.

a. Launch MX183000A with PCIe Link Training application.

b. Initialize the PCIe Link Training application.

c. Set **Specification** to **3.0(8.0 GT/s)**.

d. Click on **LEQ Test Setting** check box.
e. Select Rx LEQ tab and click on **Apply** button.

![Apply Button](image1)

f. Click on **Option** button.

![Option Button](image2)

g. Set **Algorithm** to **increment** and set **Repeat** to 11 on **Link EQ** tab.

![Option Setting](image3)
h. Reset a DUT and click on **Link Start** button.

![Diagram showing Link Start button](image1)

i. When **LTSSM State** is **Loopback.Active.Master**, click on **Saved Cursor** button on LEQ Test window.

![Diagram showing Saved Cursor button](image2)
j. If All **Saved Cursor check boxes** are **ON**, the steps are finished.

k. Launch GRL application and start **Tx EQ Response time (Cursor)**.
7. Troubleshooting

If you encounter any errors during calibration or testing, check as follows.

7.1 Calibration

7.1.1 In case of an error when calibrating Amplitude, Preset, SJ and RJ

- Check the RF connections. Especially, the connection polarity (Pos/Neg) and the trigger connections (PPG Aux Out and Scope Aux In) are easy to mistake.

- Check the software version. A different version of software may cause an unexpected error.

- Check the SigTest version. SigTest version needs to be 3.2.0 for Gen3 or 4.0.38 for Gen4. Also, this should be installed to the directory C:\Program Files (x86). Do not change the installation directory from the default setting.

7.1.2 In case of a SigTest error when performing Long Channel Calibration

A SigTest error message is displayed and SigTest has stopped working when calibrating SJ, RJ and EH/EW. Since this message is no effect on calibration results, click Close the program to continue the calibration.

To avoid this message:

- Close all applications except the GRL software, MX190000A, MX183000A and scope applications. Especially when VNC is running, SigTest may not work properly.

- If you see this message frequently despite not running other applications on the PC, use another PC with the GRL software installed.

7.1.3 When Final Eye calibration cannot be succeeded

- Use the PCIe 3.0 test fixture. It is recommended to use the calibration fixture distributed by the PCI-SIG.

- If any components (DC block, Power Divider, Attenuator and Adaptor) are attached to the Noise module output, remove them. These components may affect the waveform.
7.2  **Tx and Rx LEQ test**

7.2.1  When Tx LEQ Response cursor test cannot be started

- Before starting Rx Test, complete all calibrations or load a calibrated session file.

7.2.2  In case of a Link Training error when testing Tx LEQ Response

- Check the RF connections. Especially, the trigger connections (PPG Aux Out and Scope Aux In) are easy to mistake.

- If DUT Tx has large insertion loss, adjust the MP1900A CTLE value in the MX183000A screen. Refer to Appendix C for adjusting CTLE.

7.2.3  In case of a decode error when testing Rx LEQ Responses time

- On the **Configurations** tab, set **CTLE Setting** to **Auto**.

![Configurations Tab](image)

7.3  **Others**

7.3.1  When a session file cannot be loaded

- Close the folder where you installed the GRL software and saved PDF report file(s) because the loaded session file accesses and edits the folder.

![Load Session](image)

- While recalling the session file on CEM spec takes several minutes, it does not mean that the computer is frozen. Wait until the recalling is completed. This function recalls large waveform files which are acquired by Tx initial EQ and Tx LEQ Response time test.
Appendix

A. Quick Startup Guide

1. Connect instruments with Ethernet cables as shown below.
2. Set the IP and GPIB addresses as shown below. These can be set in the Network and Sharing Center (Windows OS feature).
3. Install all applications as shown below (Yellow letters).

- Recommended connection

![Diagram showing recommended connection]

- Optional connection

![Diagram showing optional connection]

* TCP/IP cannot be used when the GRL software is installed on a Tektronix scope. Set the GPIB address as "GPIB8::1::INSTR".

* TekVISA is needed to control Tektronix scopes. But, the PC on which TekVISA is installed cannot control Keysight scopes. Also, this configuration makes the remote control speed slower than the recommended configuration.
4. Launch application and configure equipment settings. Enter the scope address as below, and click ![image of button]. If the setting and connection are correct, the button will turn green.

**Tektronix Scope**

When the GRL software is installed on the laptop: TCPIP0::192.168.2.110::inst0::INSTR
When the GRL software is installed on the scope: GPIBX::1::INSTR

*Tektronix scope cannot use TCP/IP when the GRL software is installed on it. In this case, GPIB VISA should be set. The address can be checked using the VISA instruments Manager.*

**Keysight Scope**

When the GRL software is installed on the laptop: TCPIP0::192.168.2.110::inst0::INSTR
When the GRL software is installed on the scope: TCPIP0::localhost::inst0::INSTR

MX190000A: TCPIP0::192.168.2.100::SOCKET*
MX183000A: TCPIP0::192.168.2.100::SOCKET*

*Port numbers should be set for MX190000A and MX183000A.*
B. Before beginning Tx LEQ response time test

Before beginning Tx LEQ response time test, it is recommended to adjust the CTLE Gain value in MX183000A. Especially, this is efficient when DUT Tx has a large Insertion Loss like a System board. Also, in case of a link training error and/or bit error, adjust the CTLE Gain value.

Note:
Though the following procedure uses the screenshots for PCIe 4.0, read PCIe 4.0 as PCIe 3.0 here. Set Specification to 3.0 (8 GT/s), and then adjust the CTLE Gain value according to the following procedure.

a. In MX183000A, display the LEQ test settings and BER Measurement screen.

b. Set CTLE Gain to 0 (zero) on the BER Measurement panel.
d. On the **Tx LEQ Response** tab of the LEQ test pane, set **PPG Starting Preset** to 7, **DUT Initial Preset** to 7 and **Target Preset** to P4.

e. Click **Link Start**.

f. If the following conditions are met, adjust the **CTLE Gain** value. If there is no error, this procedure is assumed already to be done, so proceed to step h.
   - **LTSSM State** is not **Loopback, Active, Master**.
   - **Sync Header Err** is other than 0 (zero).
h. Increment the **CTLE Gain** value, and run Link Training again until **Sync Header Err** becomes 0 (zero) and **Total Error Count** becomes 0 (zero). In this case, it is considered to be error free with -6 dB.

![CTLE Gain and Error Count](image)

i. Repeat steps d to f with DUT target **Preset P7**.

j. After adjusting the **CTLE Gain** value, close the MX183000A application and return to the selector screen. The **CTLE Gain** value is stored on the MX183000A. And, start GRL Tx LEQ response time test again.