

116-Gbit/s PAM4 BERT with Industry-Best High Input Sensitivity Performance for 400GbE/800GbE

PAM4 ED MU196040B

Signal Quality Analyzer-R MP1900A series



Outline

Next-generation data centers are developing 100-Gbit/s per single-lane transmission technology for commercial deployment of 400-GbE services, resulting in increased demand for 53.125-Gbaud 4- and 8-lane PAM4 BER tests to evaluate 400-Gbit/s and future 800-Gbit/s PAM4 transceivers and devices. The Signal Quality Analyzer-R MP1900A series of high-performance BER testers (BERT) is designed to measure high-speed interfaces and supports more accurate BER evaluation of PAM4 interfaces at speeds up to 116 Gbit/s using the PAM4 ED MU196040B module with built-in clock recovery and equalizer functions for high-speed PAM4 to implement industry-leading high input sensitivity performance.

[Target Applications] 100/200/400/800GbE, CEI-112G-VSR

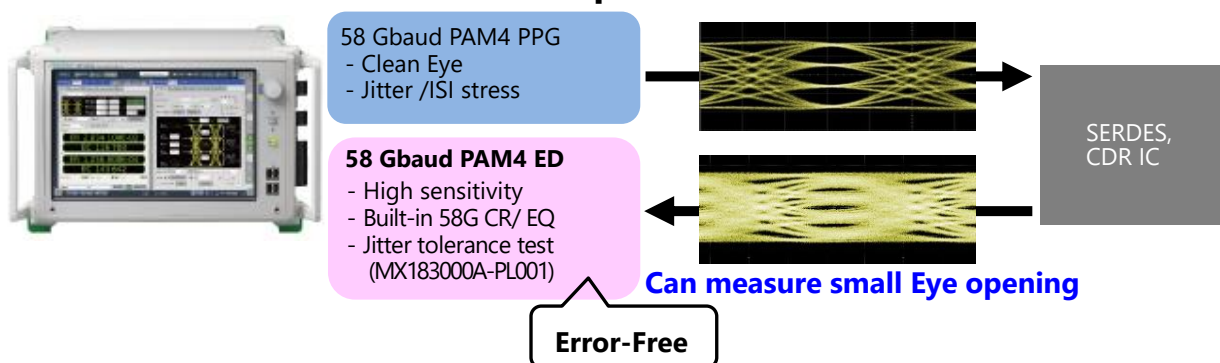
Features

- **High-performance BERT for 116-Gbit/s PAM4 error-free measurement**
Simplify previously difficult PAM4 error troubleshooting
- **Industry-leading high input sensitivity performance of 36 mV EH@53.125 Gbaud**
Support more accurate evaluations up to 116-Gbit/s PAM4
- **All-in-one 58-Gbaud PAM4 receiver test solution with built-in Clock Recovery and Equalizer functions**
Support faster testing and debugging with easy measurement system configuration
- Wideband operation: 2.4 to 64.2 Gbaud for NRZ, 2.4 to 58.2 Gbaud for PAM4
- Support CEI-112G-VSR Stressed Receiver Input Test
- Built-in 58-Gbaud PAM4 Clock Recovery
- PAM4 symbol Capture function
- Multichannel measurement (up to 4ch ED/unit)

Typical Specifications

Item	Specification
Operation Baud Rate	2.4 to 32.1 Gbaud (option 001) 2.4 to 64.2 Gbaud NRZ/2.4 to 58.2 Gbaud PAM4 (Opt-002)
Number of Inputs	2 (Data, xData)
Input Sensitivity	23 mV (Eye Height) typ. at 26.5625 Gbaud 36 mV (Eye Height) typ. at 53.125 Gbaud
Clock Recovery Range	2.4 to 29 Gbaud (Opt-021) 2.4 to 32.1 Gbaud (Opt-022) 51 to 58.2 Gbaud (extension Opt-023)
Equalizer	DFE (typ. 1.4 dB) + Low-frequency Equalizer (typ. 2 dB, response at lower than 1 GHz is controlled) (option 011)
SER Measurement	Supported (option 041)
Input Connector	V-connector (f)

Jitter Tolerance Measurement Setup



MU196040B Ordering Information

Model	Name
MU196040B	PAM4 ED
MU196040B-001	32G baud (2.4 to 32.1 Gbaud)
MU196040B-002	58G baud (2.4 to 64.2 Gbaud NRZ/ 2.4 to 58.2 Gbaud PAM4)
MU196040B-011	Equalizer
MU196040B-021	29G baud Clock Recovery (2.4 to 29 Gbaud)
MU196040B-022	32G baud Clock Recovery (2.4 to 32.1 Gbaud)
MU196040B-023	58G baud Clock Recovery Extension (51 to 58.2 Gbaud)
MU196040B-041	SER Measurement

MP1900A main unit, PPG, and software for Jitter Tolerance test

MP1900A	Signal Quality Analyzer-R
MU196020A	PAM4 PPG (Option-002, 011, 040, 042)
MU181000B	12.5GHz 4Port Synthesizer
MU181500B	Jitter Modulation Source
MX183000A-PL001	Jitter Tolerance Test