

DC Block

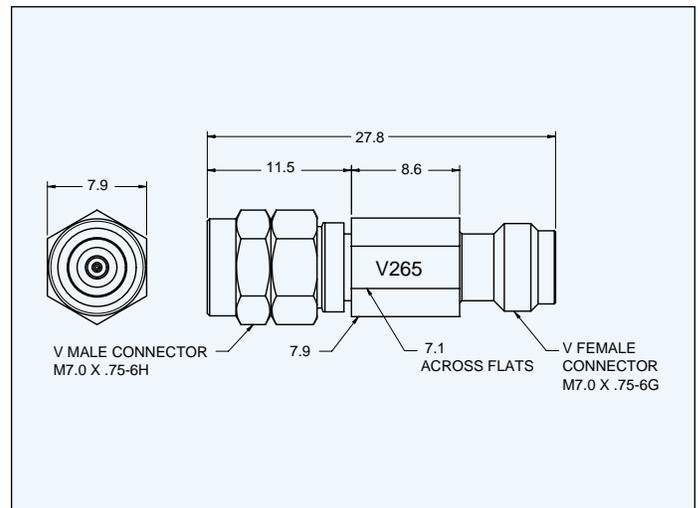
Model V265 – 50 kHz to 65 GHz



Ideal for Optical Communications and High Speed Applications

- Low Insertion Loss
- Rise Time 3 ps typical
- Ultra Wide Frequency Performance

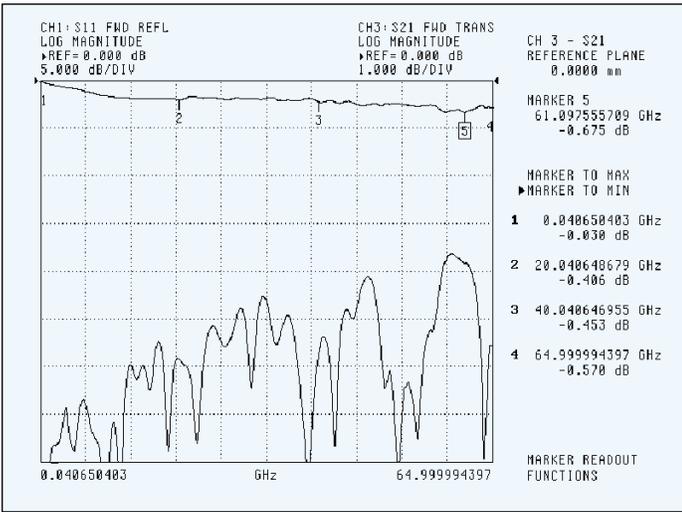
Outline Drawing



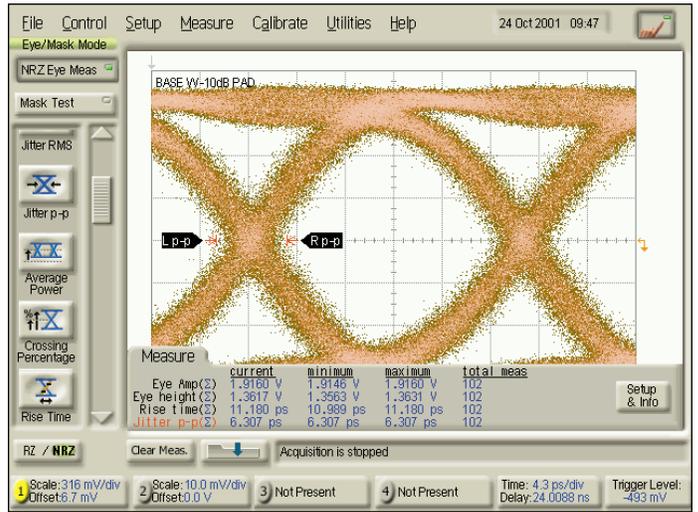
The V265 DC Block has been designed and optimized for optical communications and other high speed pulse, data or microwave applications. Based on the coaxial resilient connection – which is the same as on our V255 Gen II Bias Tee – it provides excellent low frequency response with very low losses and flat group delay over the temperature of operation. Designed to apply AC drive signals to a device while eliminating any DC voltage or current components, the V265 DC Block can be used in isolating DC leakage between two electrical components. The DC block comes with a standard V Connector® and assures excellent impedance match across the wide bandwidth available. A one-year warranty is provided.

Specifications

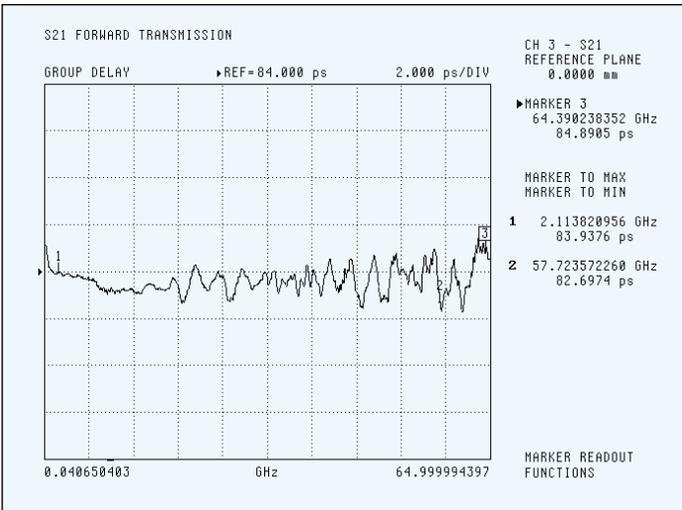
Model	Frequency range	Insertion loss	Return loss	Max RF power	Connector	Max DC voltage	Rise Time	Group delay	Operating temp.
V265	50 kHz to 65 GHz, 30 kHz to 65 GHz typical	<0.7 dB to 65 GHz typical	<-15 dB to 65 GHz typical	1 W	RF In: V(f) RF Out: V(m)	16 V	3 ps typical	84 ±2 ps typical	0°C to 80°C



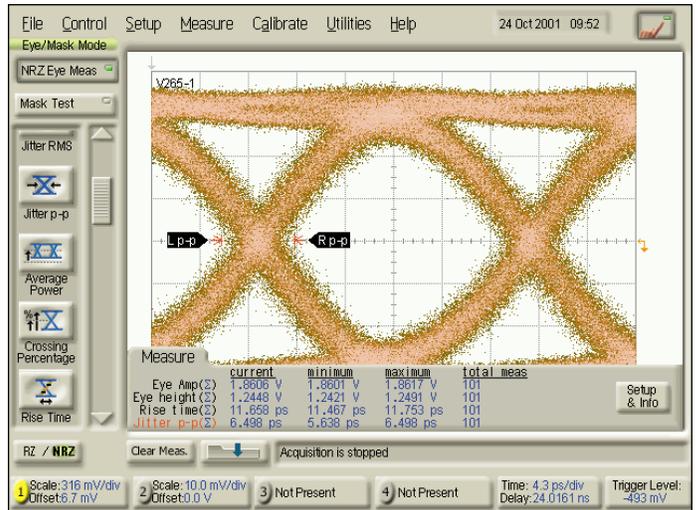
Typical High Frequency Insertion Loss and Return Loss measured on V265 over the range of 40 MHz to 65GHz using Anritsu 37397C VNA



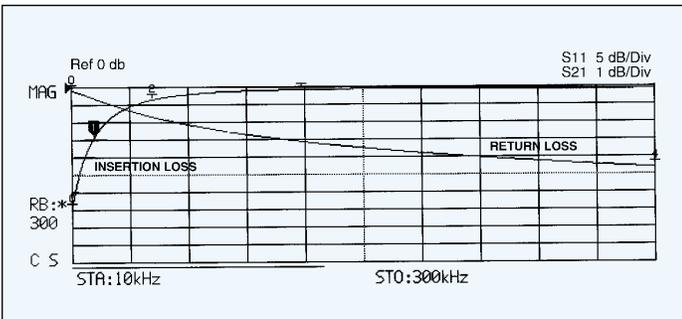
Input Test Signal to V265 2.0V NRZ Input Signal using Anritsu 43G ME7750A BERT



Typical Group Delay Performance measured on V265 using Anritsu 37397C VNA



V265 Output Response to 2.0V NRZ Input Signal using Anritsu 43G ME7750A BERT



Typical Low Frequency Insertion Loss and Return Loss measured on V265 Bias Tee over the range of 10 kHz to 300 kHz using Anritsu MS4630B Network Analyzer

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