



MX368011A

PDC Software

(For MG3681A Digital Modulation Signal Generator equipped with MU368010A TDMA Modulation Unit)

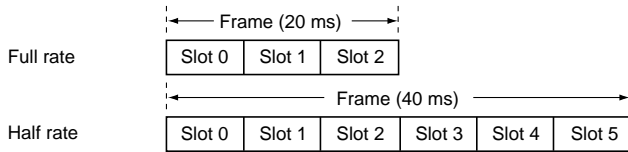


For PDC System's Receiver and Device Evaluation

Frame/Slot Configuration

Install MX368011A in MU368010A TDMA Modulation Unit, which is mounted into MG3681A Digital Modulation Signal Generator. Frame and slot configuration for selecting PDC systems conforms to full/half rate of RCR STD-27B. Slots 0 to 2 (full-rate)/0 to 5 (half-rate) can separately set to on/off. All bits except R (ramp) and G (guard) bits have PN-pattern slot configuration that can be used for device evaluation.

[Frame configuration]



[Slot configuration]

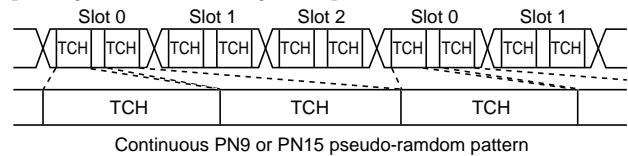
Up-link traffic channel	R	P	TCH	SW	CC	SF	SACCH	TCH	G
	4	2	112	20	8	1	15	112	6

Down-link traffic channel	R	P	TCH	SW	CC	SF	SACCH	TCH	G
	4	2	112	20	8	1	21	112	

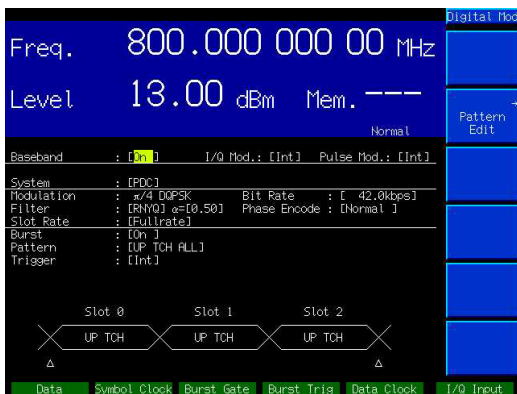
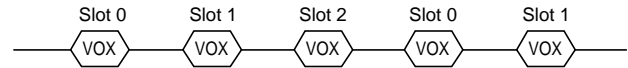
Modulation Pattern

For TCH field, continuous PN9 or PN15 pseudo-random pattern can be selected with different phase by slot. When invalid slot is received, the pseudo-random pattern becomes discontinuous so that it can be detected during error rate measurement. SW, CC and SACCH fields can be edited to any data. Scrambling can be turned on/off. MX368011A which can generate testing signal for advanced diagnosis/evaluation by itself, is ideal for testing PDC receivers or evaluation/management of devices.

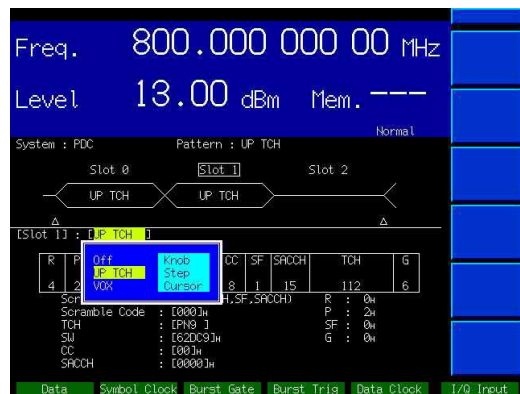
[Example of modulation pattern]



[Example of VOX signal]



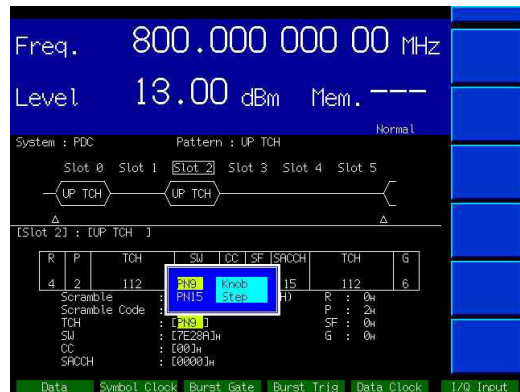
Pattern setting screen (full-rate)



Pattern editing screen (full-rate)



Pattern setting screen (half-rate)



Pattern editing screen (half-rate)

Specifications

Install MX368011A in MU368010A equipped in the MG3681A

Applicable system	PDC	
Modulation method	$\pi/4$ DQPSK	
Bit rate	Range: 37.8 to 46.2 kbps (default: 42.0 kbps), Resolution: 0.1 kbps, Accuracy: Depend on that for reference signal source of MG3681A main frame	
Baseband filter	Nyquist and root Nyquist, Roll-off factor α : 0.40 to 0.60, Resolution: 0.01	
Modulated data	Continuous modulation	PN9/PN15 pseudo-random pattern, 4-bit data repetition pattern
	Burst modulation	Data pattern conforming to RCR STD-27B can be output Frame configuration Full rate*1: Composed of 3 slots (Slot 0 to 2) [frame period: 20 ms] Half rate*2: Composed of 6 slots (Slot 0 to 5) [frame period: 40 ms] Slot configuration: For device evaluation (DEVICE)*3, up-link traffic channel (UP TCH)*4, down-link traffic channel (DOWN TCH)*5, VOX control (VOX)*6 Scramble function Turning on/off and scramble code settings are possible. Applicable slots for scramble function setting: UP TCH, DOWN TCH, VOX Scramble codes: 000 to 1FF
Auxiliary signal	Input signal	Data clock: Clock input equivalent to bit rate (input range: $\pm 1\%$ of setting value for bit rate) Data: Data input synchronized with data clock Symbol clock: Clock input for the symbol definition synchronized with data clock Burst gate: Gate signal input for burst signal on/off definition Burst trigger: Trigger signal input synchronized with frame
	Output signal	Data clock: Clock output equivalent to bit rate Data: Data output synchronized with data clock Symbol clock: Clock output for the symbol definition synchronized with data clock Burst gate: Gate signal output for burst signal on/off definition Burst trigger: Trigger signal output synchronized with frame Pattern synchronization: Signal output synchronized with PN9, PN15 or 4-bit pattern (at continuous modulation). TCH field gate, clock or RF signal on/off control signal output (at burst modulation)
I/Q signal	Output level	$\sqrt{I^2 + Q^2} = 500$ mV(rms) [bit rate: 42.0 kbps, baseband filter: root Nyquist ($\alpha = 0.5$), continuous modulation, pattern: PN9]
	Vector error	No additional function of I/Q output option (MG3681A-11): $\leq 1.6\%$ (rms) [continuous modulation, pattern: PN 9], $\leq 1.7\%$ (rms) [burst modulation, pattern: UP TCH] Installed additional function of I/Q output option (MG3681A-11): $\leq 3.0\%$ (rms) [continuous modulation, pattern: PN 9], $\leq 3.0\%$ (rms) [burst modulation, pattern: UP TCH] [bit rate: 42.0 kbps, baseband filter: root Nyquist ($\alpha = 0.5$)]
RF Signal	Frequency range	10 to 2100 MHz
	Output level range	-143 to + 13 dBm
	Level accuracy	Within ± 0.6 dB compared with CW level
	Vector error	$\leq 1.8\%$ (rms) [continuous modulation, pattern: PN9], $\leq 1.8\%$ (rms) [burst modulation, pattern: UP TCH], [bit rate: 42.0 kbps, baseband filter: root Nyquist ($\alpha = 0.5$), + 5 dBm
	Carrier leak	≤ -30 dBc [bit rate: 42.0 kbps, baseband filter: root Nyquist ($\alpha = 0.5$), continuous modulation, pattern: 0000, 18° to 35°C]
	Image rejection	≤ -40 dBc [bit rate: 42.0 kbps, baseband filter: root Nyquist ($\alpha = 0.5$), continuous modulation, pattern: 0000]
	Adjacent channel power	≤ -64 dBc/21 kHz (50 kHz offset), ≤ -68 dBc/21 kHz (100 kHz offset)*7
Burst on/off ratio	≥ 65 dB (+5 dBm)	
Firmware backup space	CPU: 280 kbyte, FPGA: 256 kbyte	

*1 Full rate	Slot 0	Slot 1	Slot 2						
*2 Half rate	Slot 0	Slot 1	Slot 2	Slot 3	Slot 4	Slot 5			
*3 For device evaluation	R 4	PN 270						G 6	
PN: PN9 or PN15									
*4 Up-link traffic channel	R 4	P 2	TCH 112	SW 20	CC 8	SF 1	SACCH 15	TCH 112	G 6
TCH: PN9/PN15, SW: 00000 to FFFFF, CC: 00 to FF, SACCH: 0000 to 7FFF									
*5 Down-link traffic channel	R 4	P 2	TCH 112	SW 20	CC 8	SF 1	SACCH 21	TCH 112	
TCH: PN9/PN15, SW: 00000 to FFFFF, CC: 00 to FF, SACCH: 000000 to 1FFFFF									
*6 VOX control	G 108	R 4	P 6	SW 20	CC 8	SF 1	SACCH 15	G 118	
SW: 00000 to FFFFF, CC: 00 to FF, SACCH: 0000 to 7FFF									

*7 Bit rate: 42.0 kbps, +5 dBm, baseband filter: root Nyquist ($\alpha = 0.5$), continuous modulation, pattern: PN 9, frequency: 810 to 958 MHz/1429 to 1501 MHz (except deterioration of performance by spurious of MG3681A main frame)

Ordering Information

Please specify model, order number, name and quantity when ordering.

Model/Order No.	Name	Remarks
MG3681A*1	Main frame Digital Modulation Signal Generator	250 kHz to 3000 MHz
MU368010A*1	Expansion unit TDMA Modulation Unit	
MX368011A	Software PDC Software	Supplied with compact flash card and PC card adapter or ATA flash memory card
W1836AE	Standard accessories MX368011A operation manual: 1 copy	

*1: Refer to catalog for the MG3681A or MU368010A

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Specifications are subject to change without notice.

ANRITSU CORPORATION MEASUREMENT SOLUTIONS

5-10-27, Minamiazabu, Minato-ku, Tokyo 106-8570, Japan
Phone: +81-3-3446-1111
Telex: J34372
Fax: +81-3-3442-0235

● U.S.A. ANRITSU COMPANY

North American Region Headquarters
1155 East Collins Blvd., Richardson, Tx 75081, U.S.A.
Toll Free: 1-800-ANRITSU (267-4878)
Phone: +1-972-644-1777
Fax: +1-972-671-1877

● Canada ANRITSU ELECTRONICS LTD.

Unit 102, 215 Stafford Road West
Nepean, Ontario K2H 9C1, Canada
Phone: +1-613-828-4090
Fax: +1-613-828-5400

● Brasil ANRITSU ELETRÔNICA LTDA.

Praia de Botafogo 440, Sala 2401 CEP 22250-040,
Rio de Janeiro, RJ, Brasil
Phone: +55-21-5276922
Fax: +55-21-537-1456

● U.K.
ANRITSU LTD.
200 Capability Green, Luton, Bedfordshire LU1 3LU, U.K.
Phone: +44-1582-433200
Fax: +44-1582-731303

● Germany
ANRITSU GmbH
Grafenberger Allee 54-56, 40237 Düsseldorf, Germany
Phone: +49-211-96855-0
Fax: +49-211-96855-55

● France
ANRITSU S.A.
9, Avenue du Québec Z.A. de Courtabœuf 91951 Les
Ulis Cedex, France
Phone: +33-1-60-92-15-50
Fax: +33-1-64-46-10-65

● Italy
ANRITSU S.p.A.
Via Elio Vittorini, 129, 00144 Roma EUR, Italy
Phone: +39-06-509-9711
Fax: +39-06-502-24-25

● Sweden
ANRITSU AB
Botvid Center, Fittja Backe 1-3 145 84 Stockholm,
Sweden
Phone: +46-853470700
Fax: +46-853470730

● Spain
ANRITSU ELECTRÓNICA, S.A.
Europa Empresarial Edificio Londres, Planta 1, Oficina
6 C/ Playa de Liencres, 2 28230 Las Rozas. Madrid,
Spain
Phone: +34-91-6404460
Fax: +34-91-6404461

● Singapore
ANRITSU PTE LTD.
6, New Industrial Rd., #06-01/02, Hoe Huat Industrial
Building, Singapore 536199
Phone: +65-282-2400
Fax: +65-282-2533

● Hong Kong
ANRITSU COMPANY LTD.
Suite 719, 7/F., Chinachem Golden Plaza, 77 Mody
Road, Tsimshatsui East, Kowloon, Hong Kong, China
Phone: +852-2301-4980
Fax: +852-2301-3545

● Korea
ANRITSU CORPORATION
14F Hyun Juk Bldg. 832-41, Yeoksam-dong,
Kangnam-ku, Seoul, Korea
Phone: +82-2-553-6603
Fax: +82-2-553-6604-5

● Australia
ANRITSU PTY LTD.
Unit 3/170 Forster Road Mt. Waverley, Victoria, 3149,
Australia
Phone: +61-3-9558-8177
Fax: +61-3-9558-8255

● Taiwan
ANRITSU COMPANY INC.
6F, 96, Sec. 3, Chien Kou North Rd. Taipei, Taiwan
Phone: +886-2-2515-6050
Fax: +886-2-2509-5519