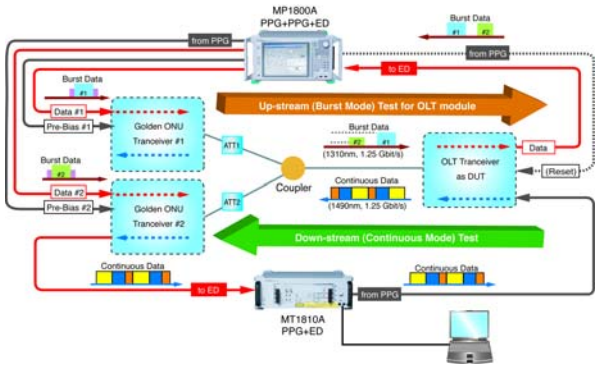
Leaflet

MX180004A

PON Application Software

What is MX1800004A?

The MX180004A PON Application Software is installed either in an external PC or the MP1800A Signal Quality Analyzer to generate PON data and auxiliary timing signals, such as Pre-Bias, Reset, etc.
ONUx2 and OLTx1 burst data and upstream tests are evaluated and performed easily using one MP1800A unit and this software. In addition, the number of evaluated ONU and OLT units can be increased by controlling several MP1800A units with this software.



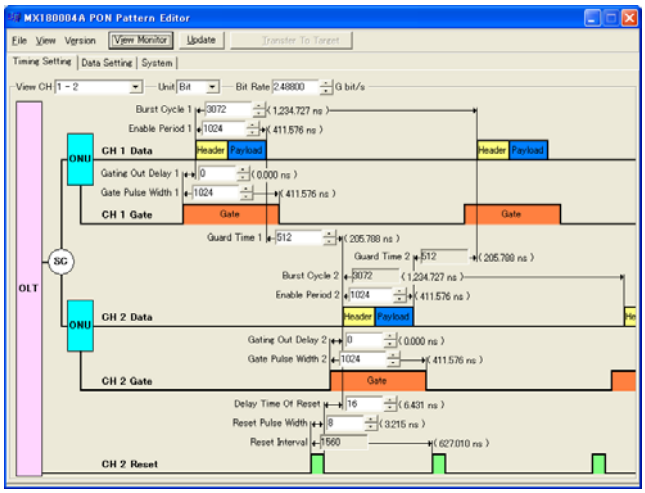
Upstream/Downstream Test Setup

10G-PON Support

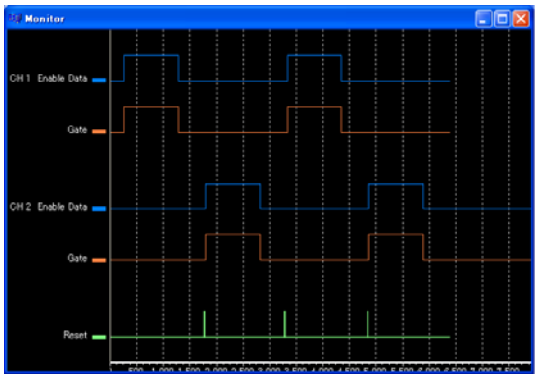
The PPG and ED modules can send and receive Burst data at frequencies between 0.1 and 12.5 Gbit/s. In addition, the 8B/10B Conversion and Scramble functions of this software simplify creation of various patterns used by each application.
Consequently, various PON applications ranging from IEEE802.3-compliant E-PON and ITU-T G.984.1/984.2-compliant G-PON to 10G-PON now being standardized as the next-generation PON can be evaluated.

Features

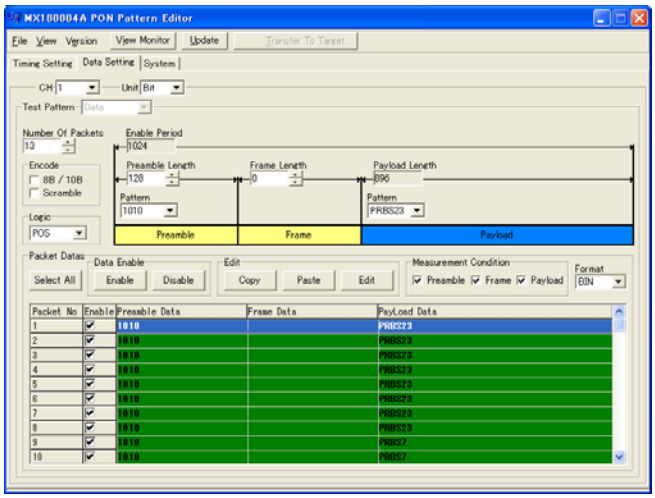
- Scramble ON/OFF function
- 8B/10B Conversion function
- User-friendly operation
- Pattern transfer over Ethernet
- Text data to internal data format converter
- Monitor function displaying patterns, Pre-Bias, and Reset signal timing relationships
- Error measurement function for specified fields, such as Header and Payload



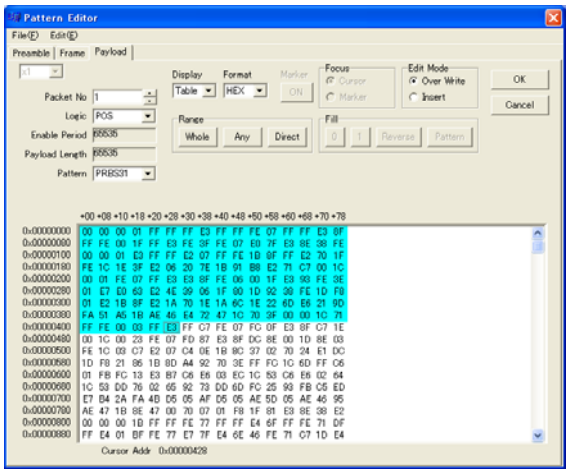
Timing Setting Screen



Timing Monitor Screen



Data Setting Screen



Pattern Editor Screen

Operating Environment and Specifications

Hardware

Item	Specification
PC	IBM-PC/AT or 100% compatible PC
CPU	300 MHz or faster Pentium®
OS	MS-Windows XP (English/Japanese)
Memory	512 MB (recommended minimum)
Monitor	Resolution: 800 x 600 pixels or better Colors: 256 or more
Hard Disk	Free space: 2 GB or more
Remote Interface	Ethernet

Specifications

Item	Specification	
Frequency	0.1 ~ 12.5 Gbit/s	0.00001-GHz Resolution
Burst Cycle	1,280 ~ 134,217,728	128-bit Resolution
Enable Period	256 ~ 8,388,608	1-bit Resolution
Gating Out Delay	0 ~ 8,192	16-bit Resolution
Gate Pulse Width	256 ~ 134,217,664	16-bit Resolution
Guard Time	0 ~ 8,192	1-bit Resolution
Delay Time of Reset	0 ~ 8,192	1- or 16-bit Resolution
Reset Pulse Width	0 ~ 8,192	1- or 16-bit Resolution
Test Pattern	Mixed/Data	
Number of Packets	1 ~ 511	
8B/10B	ON/OFF	
Scramble	ON/OFF	
Logic	POG/NEG	
Pattern	Data: PRBS 7, 9, 10, 11, 15, 20, 23, 31, ALL0, ALL1, Data	
Enable per Packets	Disable/Enable PPG Output	
Measurement Condition	Setting measurement Data Field	

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