



# PRODUCT INTRODUCTION

MP1801A/MP1802A

43.5G MUX

43.5G DEMUX

MEASUREMENT SOLUTIONS

ANRITSU CORPORATION

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# *MP1801A/1802A Product Introduction*

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## *Appearance of MP1801A/MP1802A*

**MP1801A 43.5G MUX**



**MP1802A 43.5G DEMUX**

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## ***Features of the MP1801A 43.5G MUX (Multiplexer)***

- ***Retiming processing using D-type flip-flop***  
A low-jitter, high-quality waveform that has excellent symmetry is achieved.
- ***Wide operating frequency range from 25 to 43.5 Gbit/s***  
Covers transmission speeds that support FEC (front error correction).
- ***1/4 clock delay adjustment function***  
1/4 clock output delay can be adjusted up to 120 ps. The clock signal delay, which affects the entire measurement system, can be easily adjusted from the front panel.

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## ***Features of the MP1802A 43.5G DEMUX (Demultiplexer)***

- ***Excellent input sensitivity***  
Input signals ranging from 100 mVp-p to 1.0 Vp-p can be supported.
- ***Wide operating frequency range from 25 to 43.5 Gbit/s***  
Covers transmission speed that support FEC (front error correction).
- ***Batch adjustment of delay of four clock outputs***  
The delay of four clock signals available at the rear can be adjusted with a single dial (up to 120 ps)

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## Other Features

- **Compact body**

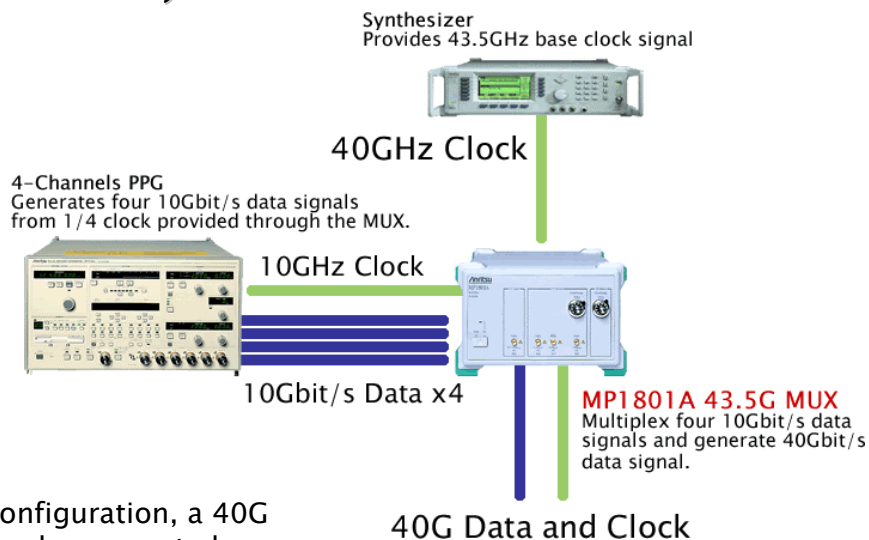
This instrument can be held in one hand, so experiments and development work do not require a large amount of space.

- **Independent configuration that enables the distance from DUT to be reduced**

Because this instrument is a compact product that is independent of the PPG and ED, the distance from the device under test (DUT) can be reduced. This enables reduction of the insertion loss that occurs in the delicate 40 Gbit/s signal lines and more accurate evaluations.

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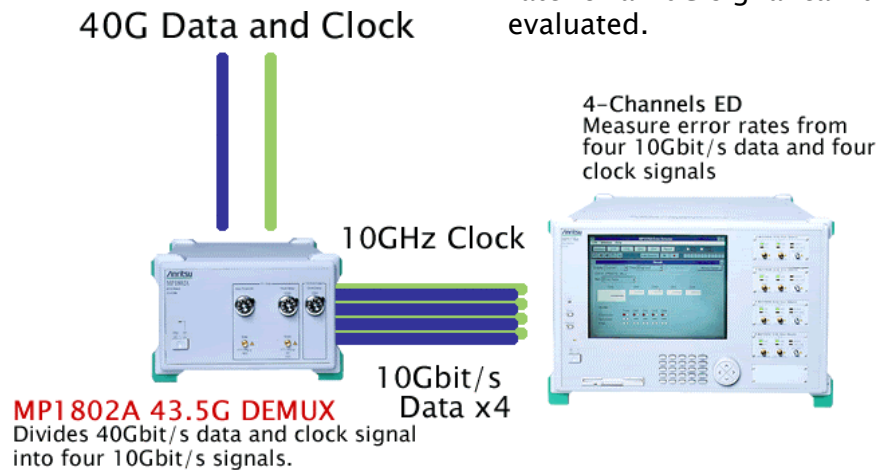
## ME7750A 43.5 Gbit/s BERT System (Transmitter)



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## ME7750A 43.5 Gbit/s BERT System (Receiver)

In this configuration, the error rate for a 40G signal can be evaluated.



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## MP1801A Front Panel

Phase adjust dial for 1/4 clock output (max. 120ps)

Phase adjust dial for 1/1 clock output (max. 120ps)

1/1 Clock output (V-connector)

1/1 Inverted data output (V-connector)

1/1 Data output (V-connector)

1/1 Clock input (V-connector)

Power switch

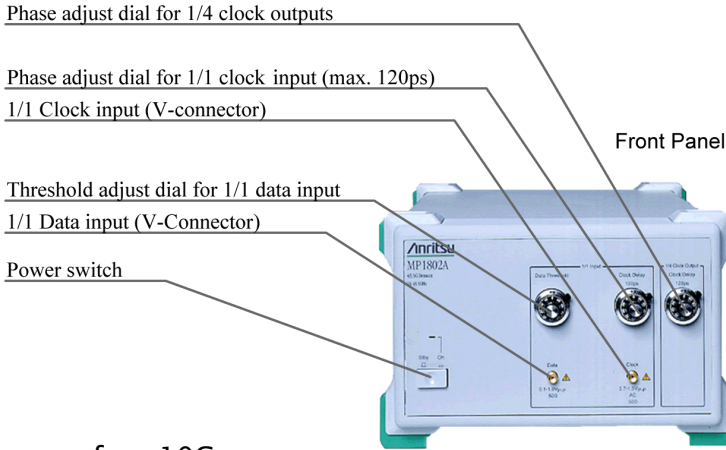
Front Panel

Four 10G data input terminals and a 1/4 clock output terminal are provided at the rear.



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# MP1802A Front Panel



Also at the rear are four 10G data output terminals and four 10G clock output terminals.





Specifications are subject to change without notice.

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