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PRODUCT INTRODUCTION

for MG3681A

MX368041A/B

W-CDMA Software

MX368041A/B-10

3GPP Release 5 Signal Pattern

MX368041A/B-11

HSDPA Signal Pattern

Update News

ANRITSU CORPORATION

MX368041A/B **W-CDMA Software (for MG3681A)**
MX368041A/B-10 **3GPP Release 5 Signal Pattern (Discontinued)**
MX368041A/B-11 **HSDPA Signal Pattern**

Update News

The basic signal pattern files based on 3GPP standard supplement the MX368041A/B Software. Moreover, the abundant signal pattern files based on Release 5 are packed by MX368041A/B-10 and MX368041A/B-11.
The signal pattern files are updated in connection with revision of 3GPP standard.

The present document introduces the following contents of the MX368041A/B Software and MX368041A/B-10/11 to the customers who already have MG3681A+MU368040A+MX368041A/B (+ MX368041A/B-10/11).

- Version-up history
- How to check Version
- How to upgrade
- File configuration in PC memory card
- Signal pattern list

MX368041A/B Version-up history

Version	Release (yyyy.mm.dd)	Contents
2.11	2005.1.11	**Patch** ● Insufficient memory error display and cancellation of waveform load correct the problem that a normal signal is not actually outputted when a waveform, which was loaded in insufficient memory, is selected although the waveform seems to have been loaded successfully.
2.10	2004. 3. 1	**Patch** ● Correction of Puncturing bit length at setting to 2 or more transport blocks in TrCH, and Convolutional coding 1/3 or 1/2 * Detailed to the other pages
Service	2004. 1.15	**Signal Pattern** (3GPP 2003-09/12 edition) ● Correction of the scrambling code of BS Test Models Carriers: fc1 fc2 fc3 fc4 Scrambling code: 0, 1, 2, 3 [HEX] ⇓ 0, 10, 20, 30 [HEX]
2.09	2003. 6.10	**Firmware improvement** ● ACLR improvement when selecting the multi-carrier signal pattern in MX368041A/B-10
2.08	2002. 8.28	**MX368041B release** ● MX368041A is the same version number as MX368041B
2.06	2002. 5.30	**Patch** ● Correction of signal output at setting to add DTX(Discontinuous Transmission) in DL RMC for BTFD * Detailed to the other pages
Service	2002. 3.20	**Signal Pattern** (3GPP 2001-12 edition) ● Standardization of TFCI bits (filled with “1”) in BS Test Models S-CCPCH (Corrected “0”)
2.05	2002. 1. 9	**Option addition** ● Maximum output level +8dB gain by MG3681A-42 RF High Level Output **Signal Pattern** (3GPP 2001-09 edition) ● Revision of OCNS parameter ● Revision of BS Test Models parameter ● Standardization of BS Test Models multi-carrier signal ● Standardization of downlink interference signal for UE RX test

2.04	2001. 7.31	**Expansion unit addition** ● AWGN combination output by MU368060A AWGN unit **Signal Pattern** (3GPP 2001-06 edition) ● Revision of OCNS parameter
2.03	2001. 5.21	Upgrade CD-ROM for W-CDMA real-time signal generation **Signal Pattern** (3GPP 2001-03 edition)

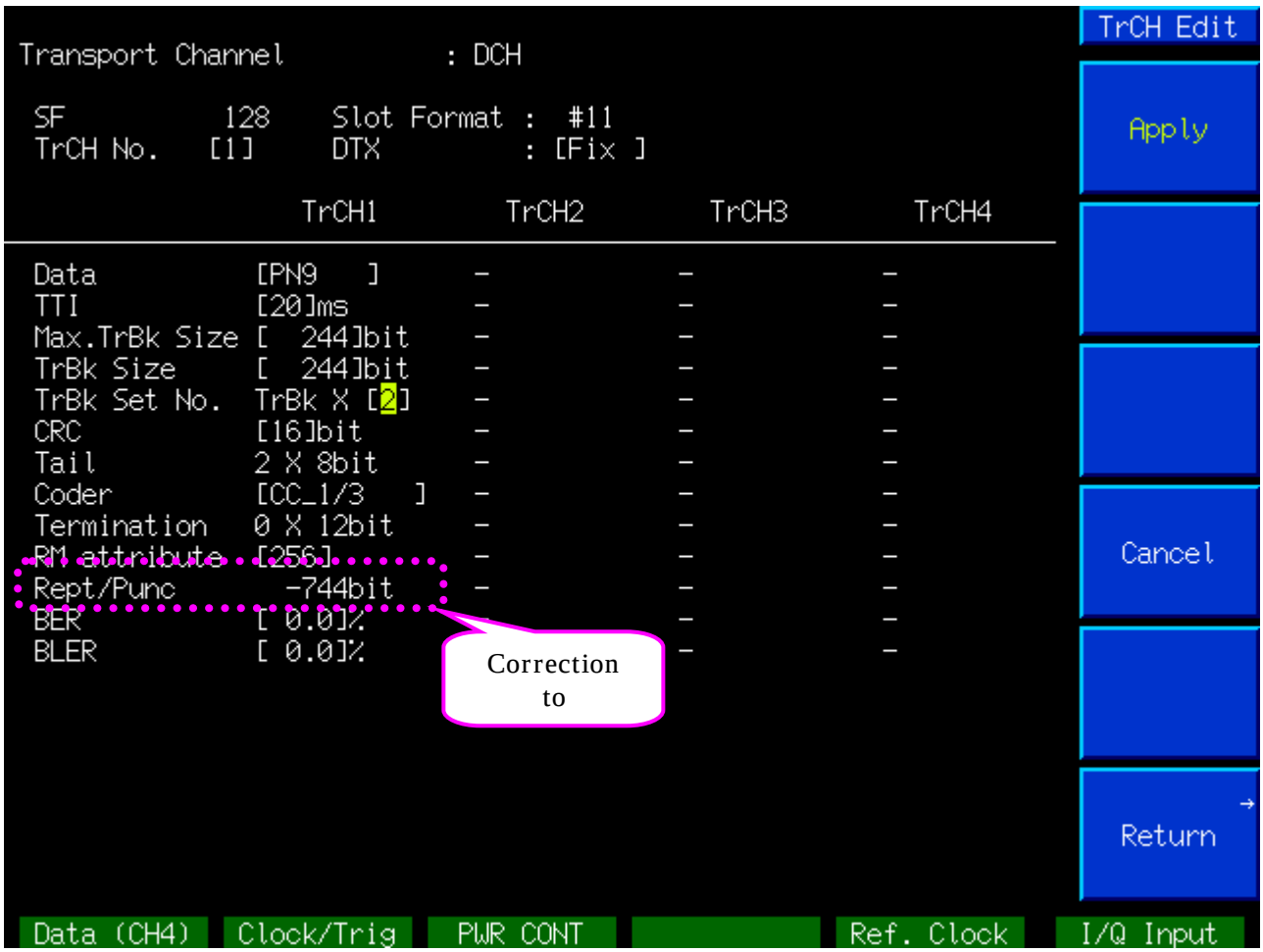
MX368041A/B-10 Version-up history

Version	Release (yyyy.mm.dd)	Contents
1.02	2004. 3. 1	**Signal Pattern** (3GPP 2003-12) ● Correction of SCH power level in BS Test Models - Test Model 1 2 carriers P-CCPCH -10 dB -10 dB P-SCH -16 dB ⇒ -13 dB S-SCH -16 dB -13 dB - Test Model 5 Single carrier, 2 carriers P-CCPCH -11 dB -11 dB P-SCH -17 dB ⇒ -14 dB S-SCH -17 dB -14 dB
1.01	2004. 1.15	**Signal Pattern** (3GPP 2003-09/12 edition) ● Correction of the scrambling code of BS Test Models Carriers: fc1 fc2 fc3 fc4 Scrambling code: 0, 1, 2, 3 [HEX] ⇓ 0, 10, 20, 30 [HEX]
1.00	2003. 6.10	**Signal Pattern** (3GPP 2003-03 edition) ● Release

MX368041A/B-11 Version-up history

Version	Release (yyyy.mm.dd)	Contents
1.01	2004. 3. 1	**Signal Pattern** (3GPP 2003-12) ● Correction of SCH power level in BS Test Models - Test Model 1 2 carriers P-CCPCH -10 dB -10 dB P-SCH -16 dB ⇒ -13 dB S-SCH -16 dB -13 dB - Test Model 5 Single carrier, 2 carriers P-CCPCH -11 dB -11 dB P-SCH -17 dB ⇒ -14 dB S-SCH -17 dB -14 dB ● Correction of 1st Rate Matching buffer length in DL FRC H-Set 4 9600 ⇒ 7200 bits
1.00	2004. 1.15	**Signal Pattern** (3GPP 2003-09/12 edition) ● Release

Correction of Puncturing bit length at setting to 2 or more transport blocks in TrCH, and Convolutional coding 1/3 or 1/2



TrBk Set No.	TrBk × [2], [3] or [4]
Coder	[CC_1/2] or [CC_1/3]

In the above case, error was detected to Puncturing bit length in Rate matching.
The output signal is also the same error.
* This setting state is not in 3GPP Reference Measurement Channel.

Correction: Bit length of TTI = (TrBk + CRC + Tail) × [1~4] × [Conv.Coding 1/R] - Puncturing
E.g. 420 × 2 = (244 + 16 + 8) × [2] × [3] - 768

⇑

Error: Bit length of TTI = {(TrBk + CRC) × [1~4] + Tail} × [Conv.Coding 1/R] - Puncturing
E.g. 420 × 2 = {(244 + 16) × [2] + 8} × [3] - 744

Correction of signal output at setting to add DTX(Discontinuous Transmission) in DL RMC for BTFD

3GPP TS 25.101 UE Radio Transmission and Reception (FDD)

8.10 Blind transport format detection

A.4 DL reference measurement channel for BTFD performance requirements

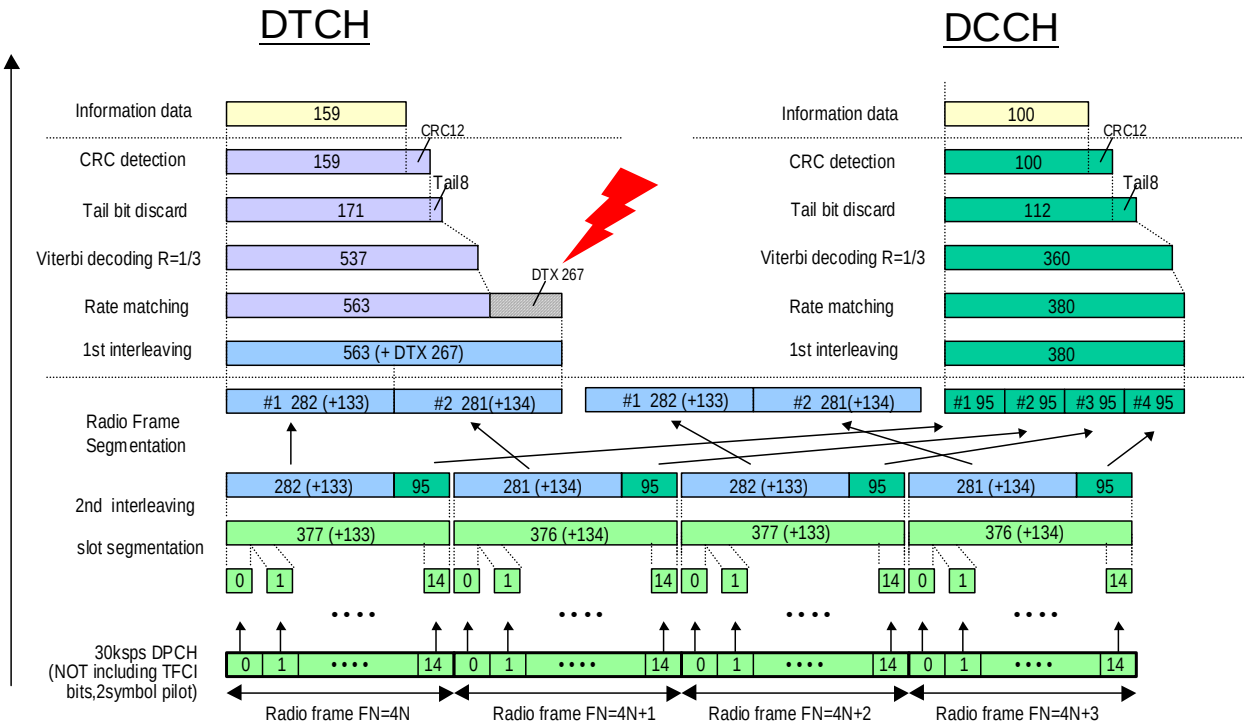


Figure A.10 (Informative): Channel coding of DL reference measurement channel for BTFD (Rate 2)

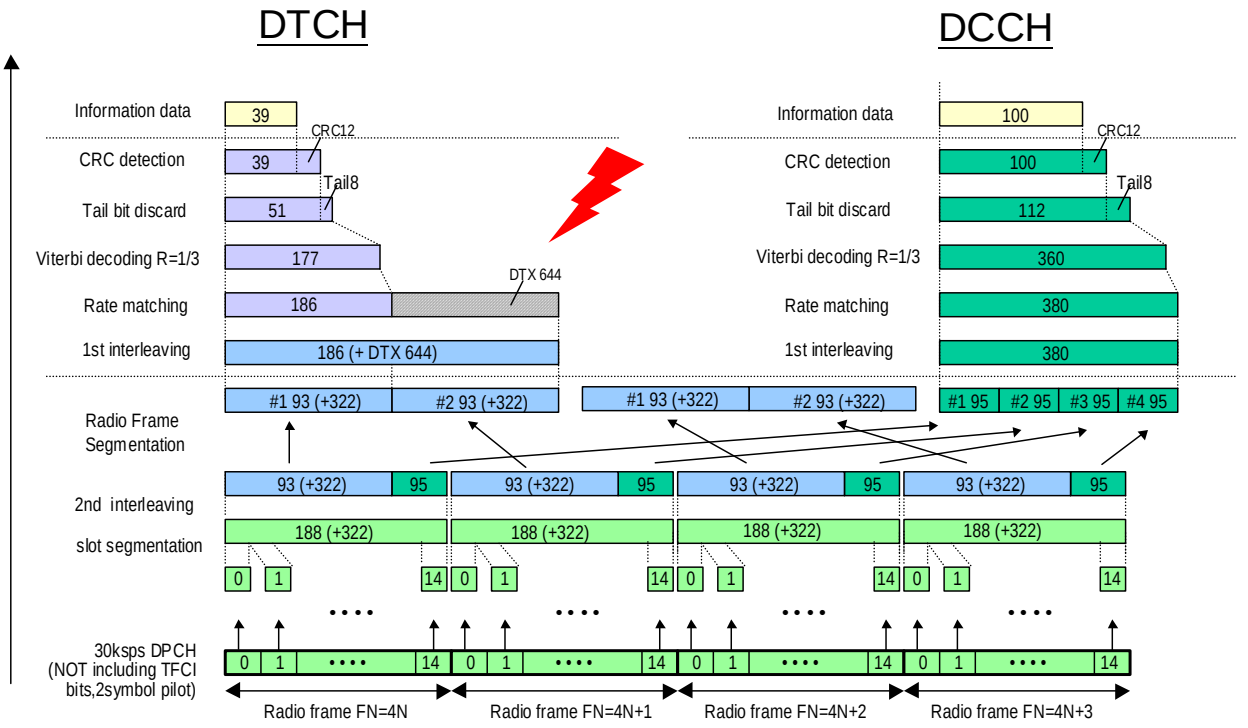
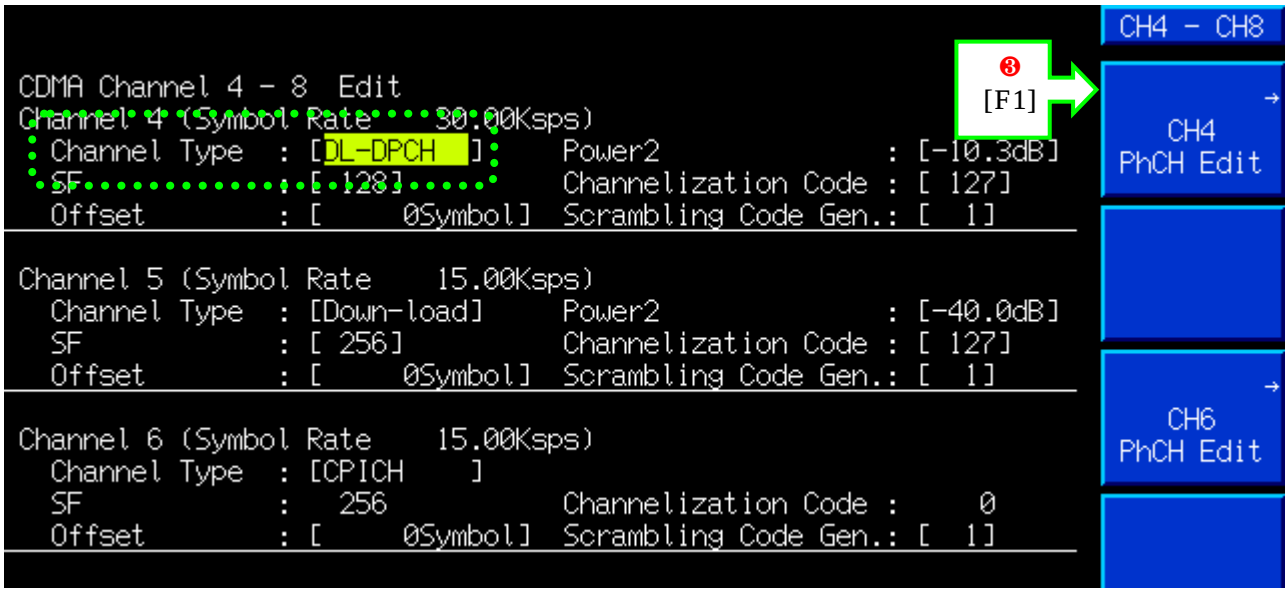
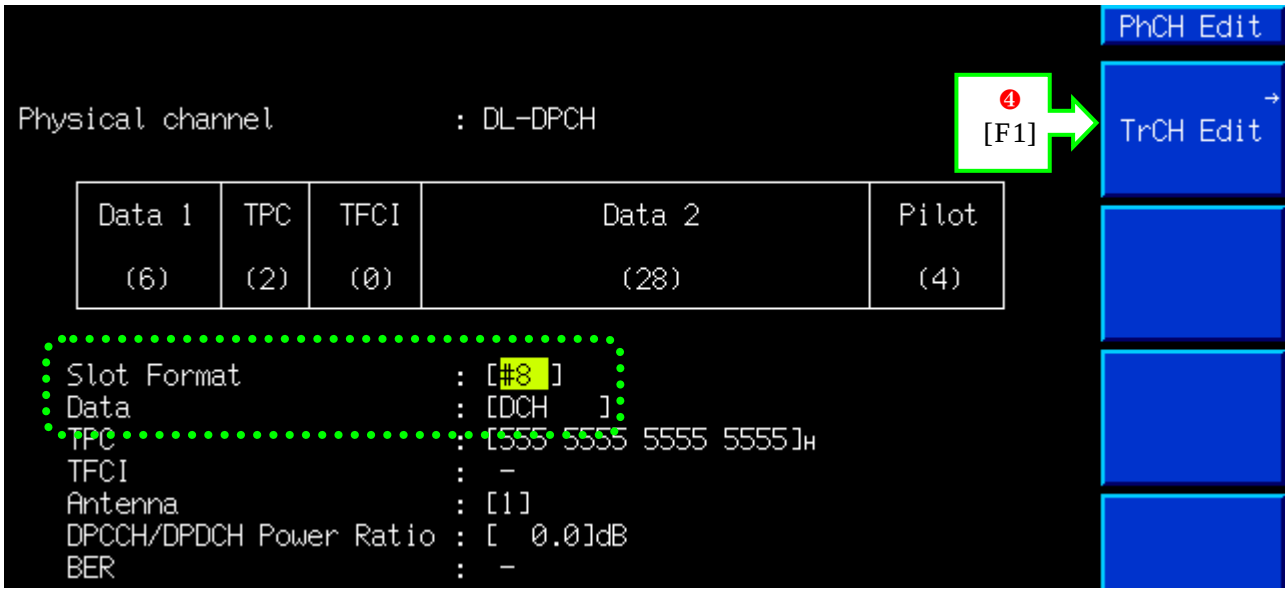


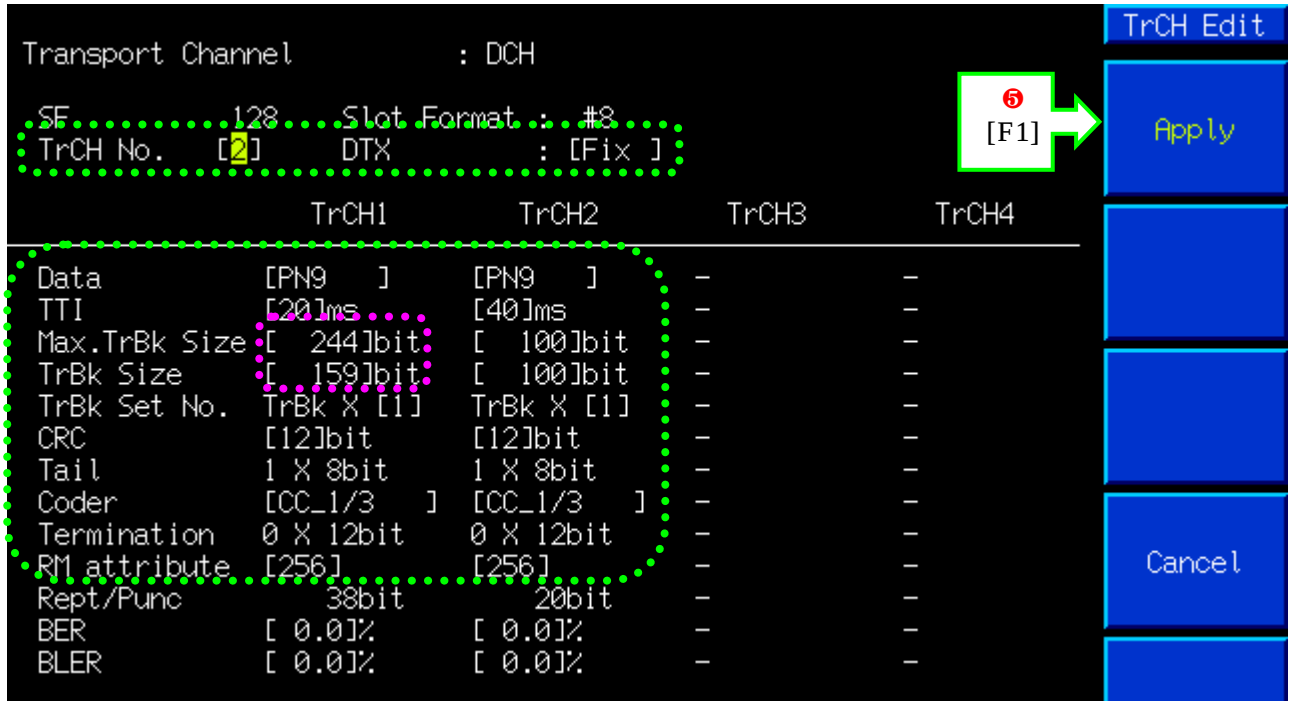
Figure A.11 (Informative): Channel coding of DL reference measurement channel for BTFD (Rate 3)

As shown in the above figure, in setting case that DTX(Discontinuous Transmission) is added to ”Rate matching” ...

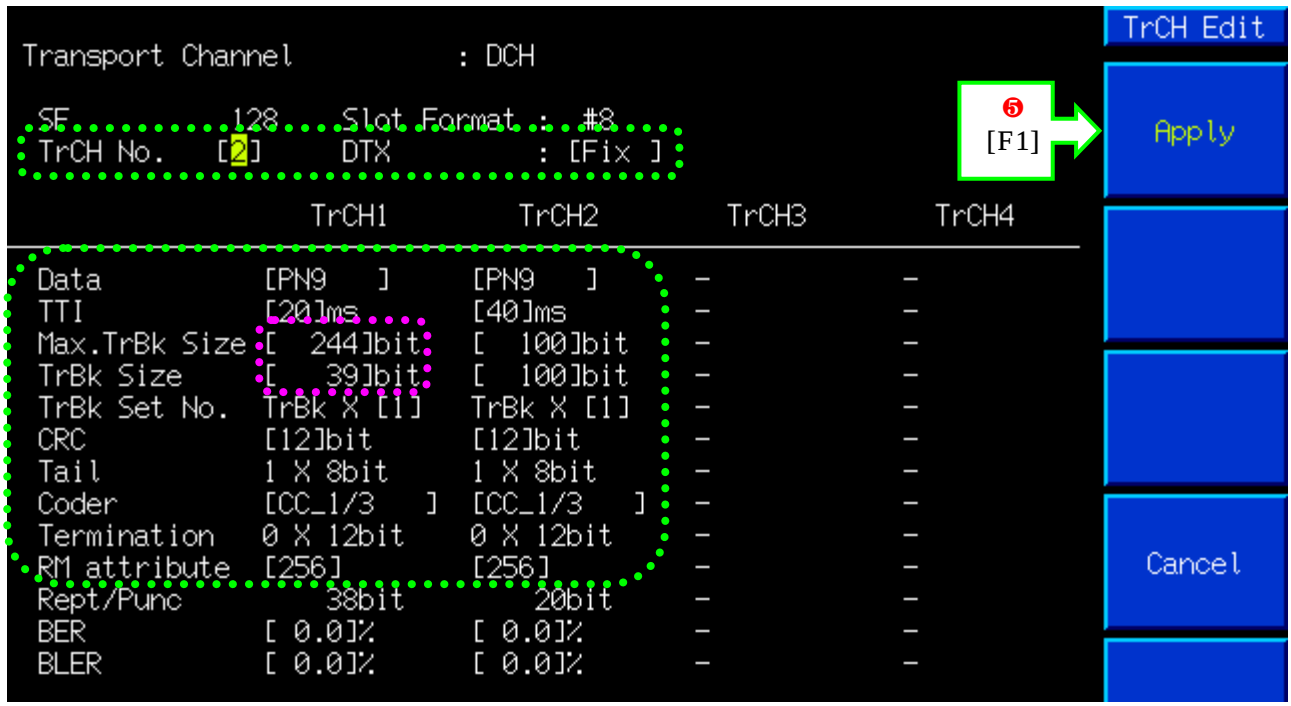

❷
[F2]
Channel
4-8
❶
[Digital Mod]


CH4 - CH8
❸
[F1]
CDMA Channel 4 - 8 Edit
Channel 4 (Symbol Rate 30.00Ksps)
Channel Type : [DL-DPCH]
SF : [128]
Offset : [0Symbol]
Power2 : [-10.3dB]
Channelization Code : [127]
Scrambling Code Gen.: [1]
Channel 5 (Symbol Rate 15.00Ksps)
Channel Type : [Down-load]
SF : [256]
Offset : [0Symbol]
Power2 : [-40.0dB]
Channelization Code : [127]
Scrambling Code Gen.: [1]
Channel 6 (Symbol Rate 15.00Ksps)
Channel Type : [CPICH]
SF : 256
Offset : [0Symbol]
Channelization Code : 0
Scrambling Code Gen.: [1]
CH4
PhCH Edit
CH6
PhCH Edit


PhCH Edit
❹
[F1]
Physical channel : DL-DPCH
Data 1 (6)
TPC (2)
TFCI (0)
Data 2 (28)
Pilot (4)
Slot Format : [#8]
Data : [DCH]
TPC : [5555 5555 5555 5555]H
TFCI : -
Antenna : [1]
DPCCH/DPDCH Power Ratio : [0.0]dB
BER : -
TrCH Edit

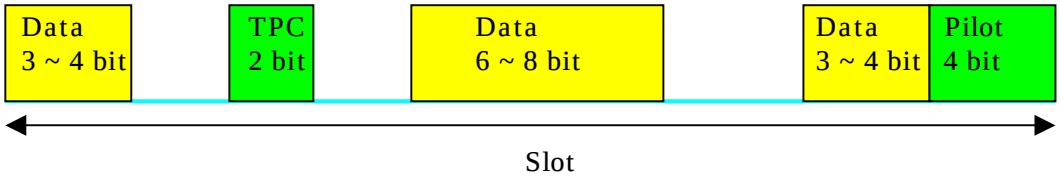


* The setting of (Rate 2)

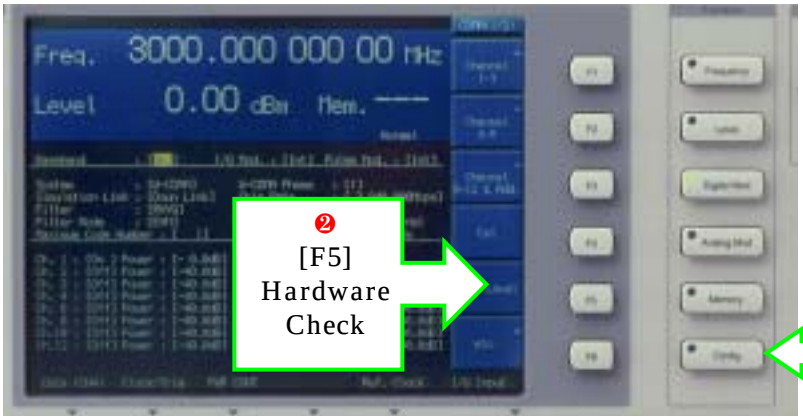


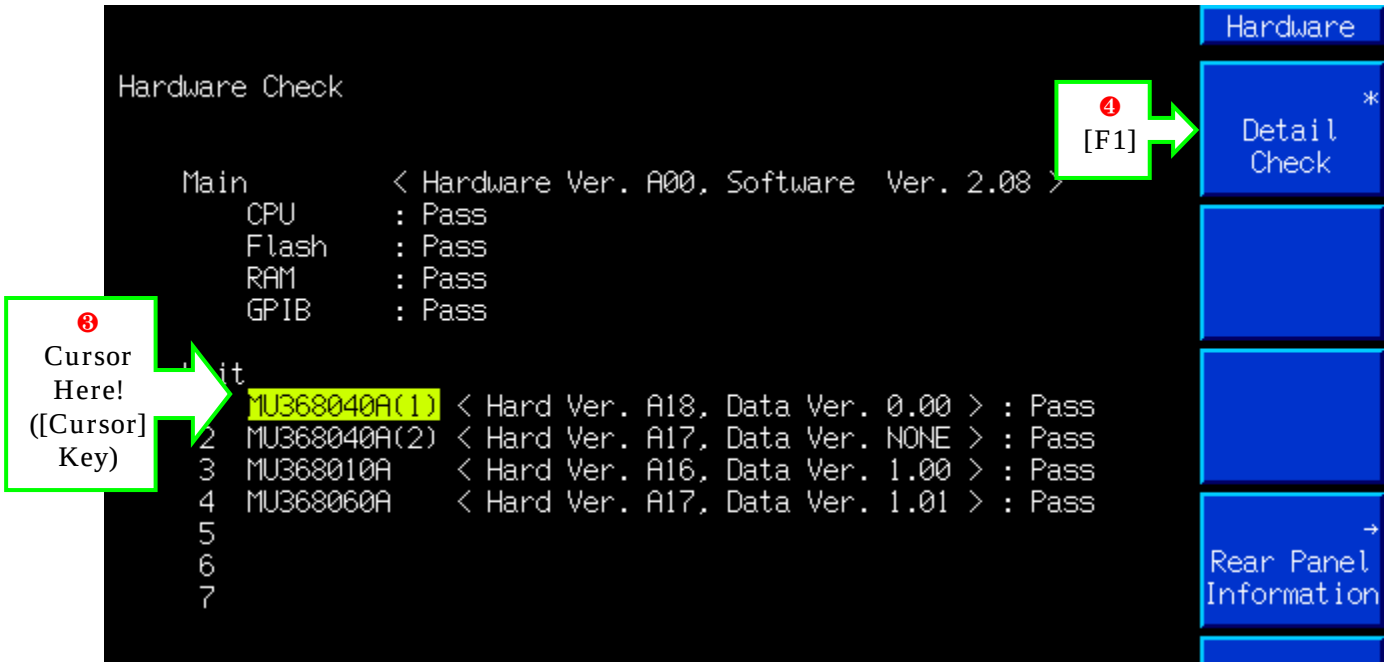
* The setting of (Rate 3)

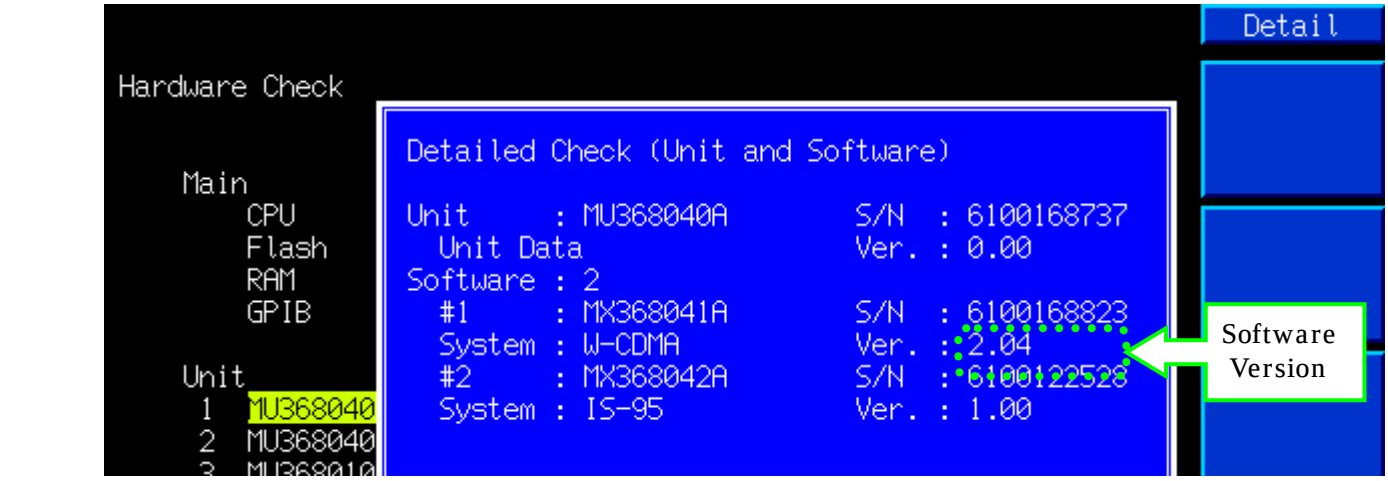
If the Data area in each Slot is odd bit length in DPCH physical channel, the I/Q symbol position of data bit “1” rotates 90 degrees data. This error was detected.
In this case, when the signal is received, it is judged that the bit is not transmitted.



How to check Version



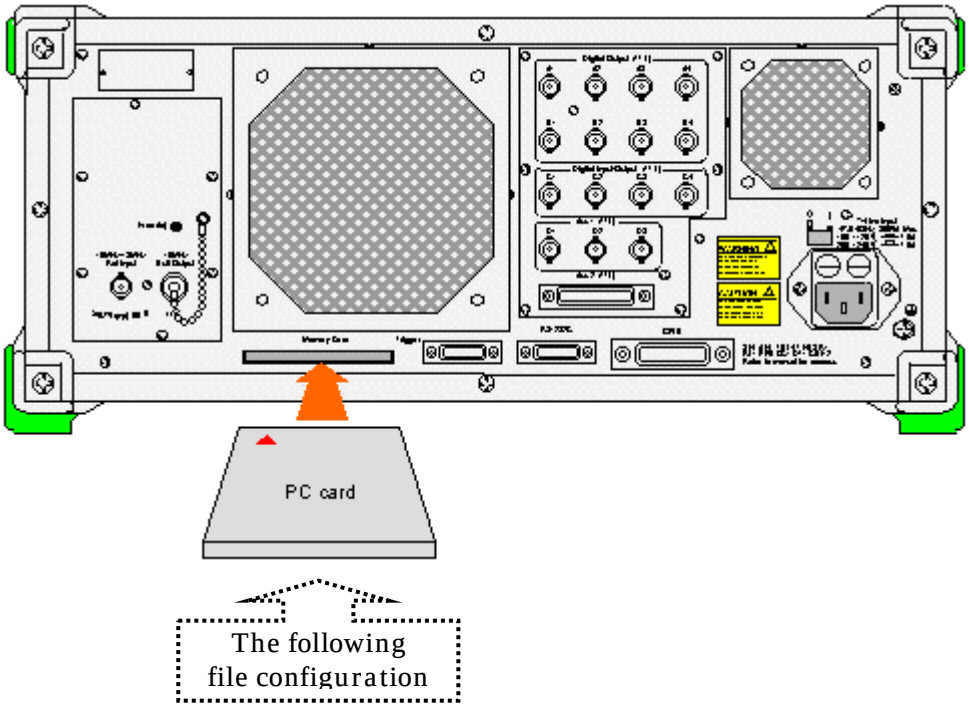




How to upgrade

Please consult with our sales staff about updated firmware object.

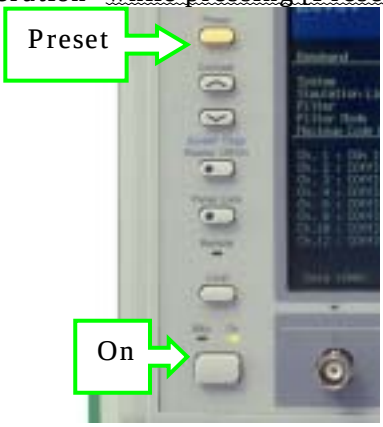
1. Please prepare clear PC memory card (MX368041A/B card that the file and folder are empty).
2. Please copy to PC memory card like the following page.
3. Please power-off MG3681A.
4. Please insert PC memory card in [Memory Card] on the rear panel of MG3681A.



[Updated software object]

5. Please power-on MG3681A (while pressing [Preset]).
   (All LED on a front panel light up simultaneously. → Release the [Preset].)

DSP program has been updated by Version 2.06, so in case of the upgrade from Version 2.05 or less, operation "while pressing [Preset]" is required.



Power-on while pressing [Preset] is changed to the state of Master Reset (initial setting in the factory).
The data of BPM(Basic Parameter Memory) and APM(All Parameter Memory) saved by [Memory] function are erased, so Memory data should be backed up to a PC memory card if necessary.

1) Backup to a PC memory card:
   [Memory] → [F6]etc. → [F3]Basic PRM Export / [F5]All PRM Export
2) Download from a PC memory card:
   [Memory] → [F6]etc. → [F2]Basic PRM Import / [F4]All PRM Import

6. The firmware is upgraded in 1 to 2 minutes.

File configuration in PC memory card

The signal pattern files are saved in each folder  .


*****.cmb *****.dat *****.dli


*****.txt

MX368041A/B Version 2.10


DL2002-3-99BASIC
Folder : The latest extracted basic signal pattern files
(25 patterns Red characters)


DL2002-3-99FULLSET
Folder : The latest full signal pattern files (62 patterns)


Manritsu
Folder : Firmware


*****.cmb *****.dat *****.dli


*****.txt
Files : The same extracted basic signal pattern files as the inside of the folder
(25 patterns Red characters)

MX368041A/B-10 Version 1.02


BS-TestModel1
Folder : TS25.141 Test Model 1 Single carrier (12 patterns)


BS-TestModel1MC
Folder : TS25.141 Test Model 1 2 carriers (6 patterns Blue characters)


BS-TestModel2
Folder : TS25.141 Test Model 2 (4 patterns)


BS-TestModel3
Folder : TS25.141 Test Model 3 (8 patterns)


BS-TestModel4
Folder : TS25.141 Test Model 4 (4 patterns)


BS-TestModel5
Folder : TS25.141 Test Model 5 Single carrier (12 patterns Blue characters)


BS-TestModel5MC
Folder : TS25.141 Test Model 5 2 carriers (6 patterns Blue characters)


DL-RMC
Folder : TS25.101 DL RMC (13 patterns)


DL-Interferer
Folder : TS25.101 W-CDMA Modulated Interferer (1 patterns)


DL-RMC4PILOT
Folder : TS25.101 DPCCH as phase reference (3 patterns Blue characters)

DL-OTD
Folder : TS25.101 Open-loop Transmit diversity mode (6 patterns Blue char.)

DL-Compressed
Folder : TS25.101 Downlink compressed mode (15 patterns Blue characters)

DL-BTFD
Folder : TS25.101 BTFD (9 patterns Blue characters)

DL-PCH
Folder : TS25.101 PCH (3 patterns Blue characters)

DL-AMR
Folder : Downlink AMR (9 patterns)

DL-ISDN
Folder : Downlink ISDN (3 patterns)

UL-RMC
Folder : TS25.104 UL RMC (4 patterns)

UL-RACH_CPCH
Folder : TS25.104 RACH / CPCH (5 patterns Blue characters)

UL-SSDT
Folder : TS25.104 SSDT (2 patterns Blue characters)

UL-AMR
Folder : Uplink AMR (3 patterns)

UL-ISDN
Folder : Uplink ISDN (1 patterns)

MX368041A/B-11 Version 1.01

BS-TestModel1
Folder : TS25.141 Test Model 1 Single carrier (12 patterns)

BS-TestModel1MC
Folder : TS25.141 Test Model 1 2 carriers (6 patterns Blue characters)

BS-TestModel2
Folder : TS25.141 Test Model 2 (4 patterns)

BS-TestModel3
Folder : TS25.141 Test Model 3 (8 patterns)

BS-TestModel4
Folder : TS25.141 Test Model 4 (4 patterns)

BS-TestModel5
Folder : TS25.141 Test Model 5 Single carrier (12 patterns Blue characters)

BS-TestModel5MC
Folder : TS25.141 Test Model 5 2 carriers (6 patterns Blue characters)

DL-RMC
Folder : TS25.101 DL RMC (13 patterns)

DL-Interferer

Folder : TS25.101 W-CDMA Modulated Interferer (1 patterns)

DL-RMC4PILOT

Folder : TS25.101 DPCCH as phase reference (3 patterns Blue characters)

DL-OTD

Folder : TS25.101 Open-loop Transmit diversity mode (6 patterns Blue char.)

DL-Compressed

Folder : TS25.101 Downlink compressed mode (15 patterns Blue characters)

DL-BTFD

Folder : TS25.101 BTFD (9 patterns Blue characters)

DL-PCH

Folder : TS25.101 PCH (3 patterns Blue characters)

DL-FRC

Folder : TS25.101 DL FRC (8 patterns Green characters)

DL-AMR

Folder : Downlink AMR (9 patterns)

DL-ISDN

Folder : Downlink ISDN (3 patterns)

UL-RMC

Folder : TS25.104 UL RMC (4 patterns)

UL-RACH_CPCH

Folder : TS25.104 RACH / CPCH (5 patterns Blue characters)

UL-SSDT

Folder : TS25.104 SSDT (2 patterns Blue characters)

UL-AMR

Folder : Uplink AMR (3 patterns)

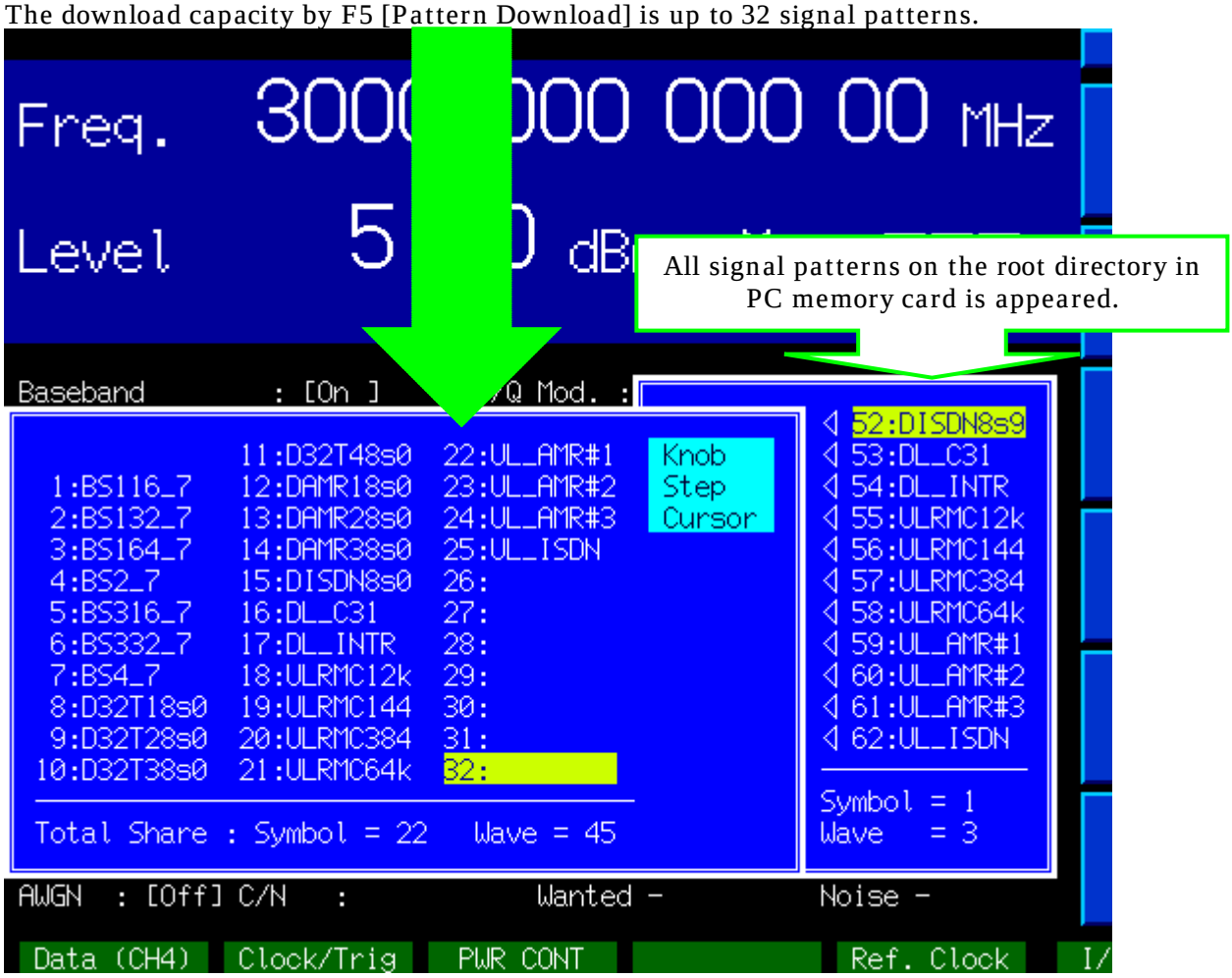
UL-ISDN

Folder : Uplink ISDN (1 patterns)

In order to download the signal patterns by F5 [Pattern Download], please copy the required signal pattern files to the root directory in a PC memory card.

The already downloaded unnecessary signal patterns can be deleted by F4 [Pattern Clear].

The download capacity by F5 [Pattern Download] is up to 32 signal patterns.



Signal pattern list

Contents

- Release 3GPP specifications release date
- Title 3GPP specifications name
- Number 3GPP specifications number
- Test item Type of test
- Channel combination Type of channel combination to output
- DPCH parameter Type of DPCH parameter
- Signal pattern name Signal pattern display name

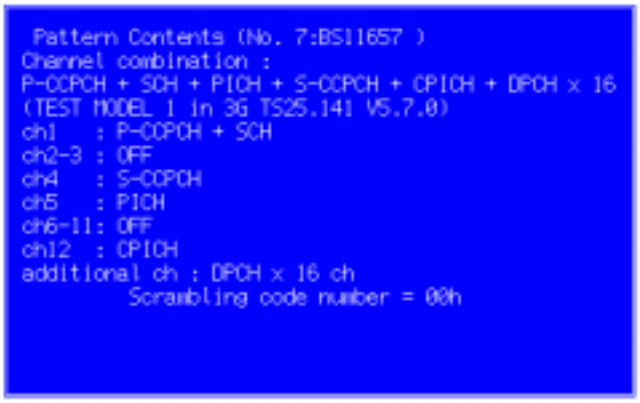
Release			2001-03	2001-06	2001-09	2001-12	2002-03	2002-06	2002-09	2002-12	2003-03	2003-06	2003-09	2003-12
Title		Number	Release 1999 (upside) / Release 5 (downside)											
Physical Channel Format		TS25.211	V3.6.0	V3.7.0	V3.8.0	V3.9.0	V3.10.0	V3.11.0	V3.12.0 V5.2.0	- V5.3.0	- -	- V5.4.0	- V5.5.0	- -
Channel Coding		TS25.212	V3.5.0	V3.6.0	V3.7.0	V3.8.0	V3.9.0	V3.10.0	V3.11.0 V5.2.0	- V5.3.0	- V5.4.0	- V5.5.0	- V5.6.0	- V5.7.0
Spreading and modulation		TS25.213	V3.5.0	V3.6.0	-	V3.7.0	-	V3.8.0	- V5.2.0	- -	- V5.3.0	- -	- V5.4.0	V3.9.0 V5.5.0
UE Radio Transmission and Reception (FDD)		TS25.101	V3.6.0	V3.7.0	V3.8.0	V3.9.0	V3.10.0	V3.11.0	- V5.4.0	V3.12.0 V5.5.0	V3.13.0 V5.6.0	V3.14.0 V5.7.0	V3.15.0 V5.8.0	V3.16.0 V5.9.0
UTRA(BS) FDD; Radio transmission and reception		TS25.104	V3.6.0	V3.7.0	V3.8.0	V3.9.0	V3.10.0	-	- V5.4.0	V3.11.0 V5.5.0	V3.12.0 V5.6.0	- V5.7.0	- -	- V5.8.0
Base station conformance testing (FDD)		TS25.141	V3.5.0	V3.6.0	V3.7.0	V3.8.0	V3.9.0	V3.10.0	V3.11.0 V5.4.0	V3.12.0 V5.5.0	V3.13.0 V5.6.0	- V5.7.0	- -	- V5.8.0
Channel coding and multiplexing examples		TR25.944	V3.4.0	V3.5.0	-	-	-	-	-	-	-	-	-	-
Test item	Channel combination	DPCH parameter	Signal pattern name											
TS25.141 6.1.1 Transmitter Test Models	TS25.141 6.1.1.1 Test Model 1	16 DPCH	BS1_16	-	BS116_7 BS116_71 BS116_72 BS116_73	BS116_8 BS116_81 BS116_82 BS116_83	-	-	-	-	-	-	BS11657 BS116571 BS116572 BS116573	-
		(2 carriers)							B11654d B11654d2	B11655d B11655d2	-	-	B11657d B11657d2	B11657d B11657d2
		32 DPCH	BS1_32	-	BS132_7 BS132_71 BS132_72 BS132_73	BS132_8 BS132_81 BS132_82 BS132_83	-	-	-	-	-	-	BS13257 BS132571 BS132572 BS132573	-
		(2 carriers)							B13254d B13254d2	B13255d B13255d2	-	-	B13257d B13257d2	B13257d B13257d2
		64 DPCH	BS1_64	-	BS164_7 BS164_71 BS164_72 BS164_73	BS164_8 BS164_81 BS164_82 BS164_83	-	-	-	-	-	-	BS16457 BS164571 BS164572 BS164573	-
		(2 carriers)							B16454d B16454d2	B16455d B16455d2	-	-	B16457d B16457d2	B16457d B16457d2
	TS25.141 6.1.1.2 Test Model 2	-	BS2	-	BS2_7 BS2_71 BS2_72 BS2_73	BS2_8 BS2_81 BS2_82 BS2_83	-	-	-	-	-	-	BS257 BS2571 BS2572 BS2573	-
	TS25.141 6.1.1.3 Test Model 3	16 DPCH	BS3_16	-	BS316_7 BS316_71 BS316_72 BS316_73	BS316_8 BS316_81 BS316_82 BS316_83	-	-	-	-	-	-	BS31657 BS316571 BS316572 BS316573	-
		32 DPCH	BS3_32	-	BS332_7 BS332_71 BS332_72 BS332_73	BS332_8 BS332_81 BS332_82 BS332_83	-	-	-	-	-	-	BS33257 BS332571 BS332572 BS332573	-

	TS25.141 6.1.1.4 Test Model 4	-	BS4	-	BS4_7 BS4_71 BS4_72 BS4_73	BS4_8 BS4_81 BS4_82 BS4_83	-	-	-	-	-	-	BS457 BS4571 BS4572 BS4573	-
	TS25.141 6.1.1.4A Test Model 5	2 HS-PDSCH + 6 DPCH							BS5_254 BS5_2541 BS5_2542 BS5_2543	BS5_255 BS5_2551 BS5_2552 BS5_2553	-	-	BS5_257 BS5_2571 BS5_2572 BS5_2573	BS5_257 BS5_2571 BS5_2572 BS5_2573
		(2 carriers)							B5_254d B5_254d2	B5_255d B5_255d2	-	-	B5_257d B5_257d2	B5_257d B5_257d2
		4 HS-PDSCH + 14 DPCH							BS5_454 BS5_4541 BS5_4542 BS5_4543	BS5_455 BS5_4551 BS5_4552 BS5_4553	-	-	BS5_457 BS5_4571 BS5_4572 BS5_4573	BS5_457 BS5_4571 BS5_4572 BS5_4573
		(2 carriers)							B5_454d B5_454d2	B5_455d B5_455d2	-	-	B5_457d B5_457d2	B5_457d B5_457d2
		8 HS-PDSCH + 30 DPCH							BS5_854 BS5_8541 BS5_8542 BS5_8543	BS5_855 BS5_8551 BS5_8552 BS5_8553	-	-	BS5_857 BS5_8571 BS5_8572 BS5_8573	BS5_857 BS5_8571 BS5_8572 BS5_8573
		(2 carriers)							B5_854d B5_854d2	B5_855d B5_855d2	-	-	B5_857d B5_857d2	B5_857d B5_857d2
TS25.101	TS25.101 Annex C.3.1	TS25.101 Annex A.3.1	DL_C31	-	-	-	-	-	-	-	-	-	-	-
7 Receiver characteristics 8 Performance requirement	TS25.101 Annex C.3.2		DL_C32T1	D32T17s0	D32T18s0 D32T18s8 D32T18s9	-	-	-	-	-	-	-	-	-
		TS25.101 Annex A.3.2	DL_C32T2	D32T27s0	D32T28s0 D32T28s8 D32T28s9	-	-	-	-	-	-	-	-	-
		TS25.101 Annex A.3.3	DL_C32T3	D32T37s0	D32T38s0 D32T38s8 D32T38s9	-	-	-	-	-	-	-	-	-
		TS25.101 Annex A.3.4	DL_C32T4	D32T47s0	D32T48s0 D32T48s8 D32T48s9	-	-	-	-	-	-	-	-	-
	TS25.101 Annex C.4	TS25.101 Table C.6	-	-	DL_INTR	-	-	-	-	-	-	-	-	-
8.3 in multi-path (Case7) Test21~25	TS25.101 Annex C.3.5	TS25.101 Annex A.4A							4Ps0 4Ps8 4Ps9	-	-	-	-	-
8.6.1 open-loop transmit diversity	TS25.101 Annex C.3.3	TS25.101 Annex A.3.1							OTD1s0 OTD1s8 OTD1s9	-	-	-	-	-
									OTD2s0 OTD2s8 OTD2s9	-	-	-	-	-
8.9 Downlink compressed mode	TS25.101 Annex C.3.2	TS25.101 Annex A.5							DCP11540 DCP11548 DCP11549	-	-	-	-	-

		(Downlink)							DCP12540 DCP12548 DCP12549	-	-	-	-	-
									DCP21540 DCP21548 DCP21549	-	-	-	-	-
									DCP22540 DCP22548 DCP22549	-	-	-	-	-
									DCP23540 DCP23548 DCP23549	-	-	-	-	-
8.10 BTFD		TS25.101 Annex A.4							BTFD1s0 BTFD1s8 BTFD1s9	-	-	-	-	-
									BTFD2s0 BTFD2s8 BTFD2s9	-	-	-	-	-
									BTFD3s0 BTFD3s8 BTFD3s9	-	-	-	-	-
8.12 PCH		TS25.101 Annex A.6							PCHs0 PCHs8 PCHs9	-	-	-	-	-
7.4.2 Maximum input level HS-PDSCH 9 Performance requirement (HSDPA)	TS25.101 Annex C.5.1 Table C.8	TS25.101 Annex A.7.1											F1P0s0 F1A0s0 F2P0s0 F2A0s0 F3P0s0 F3A0s0 F4P0s0 F5P0s0	F1P0s0 F1A0s0 F2P0s0 F2A0s0 F3P0s0 F3A0s0 F4P0s0 F5P0s0
TS25.104	TS25.104 Annex A.1	TS25.104 Annex A.2	ULRMC12k	-	-	-	-	-	-	-	-	-	-	-
7 Receiver characteristics		TS25.104 Annex A.3	ULRMC64k	-	-	-	-	-	-	-	-	-	-	-
8 Performance requirement		TS25.104 Annex A.4	ULRMC144	-	-	-	-	-	-	-	-	-	-	-
		TS25.104 Annex A.5	ULRMC384	-	-	-	-	-	-	-	-	-	-	-
8.8.1 RACH preamble	TS25.211 5.2.2.1	TS25.213 4.3.3							PRE	-	-	-	-	-
8.8.3 RACH message		TS25.104 Annex A.7							R168 R360	- -	- -	- -	- -	- -
8.9.3 CPCH message	TS25.211 5.2.2.2	TS25.104 Annex A.8							C168 C360	- -	- -	- -	- -	- -
8.10 SSDT	TS25.104 Annex A.1	TS25.104 Annex A.2							SSDTa SSDTb	- -	- -	- -	- -	- -
-	TS25.101 Annex C.3.2	TR25.944 4.1.1.3.1.1 DCCH 4.1.1.3.1.2 AMR TFCS#1 4.1.1.3.2.2	DL_AMR#1	DAMR17s0	DAMR18s0 DAMR18s8 DAMR18s9	-	-	-	-	-	-	-	-	-

		TR25.944 4.1.1.3.1.1 DCCH 4.1.1.3.1.2 AMR TFCS#2 4.1.1.3.2.2	DL_AMR#2	DAMR27s0	DAMR28s0 DAMR28s8 DAMR28s9	-	-	-	-	-	-	-	-	-
		TR25.944 4.1.1.3.1.1 DCCH 4.1.1.3.1.2 AMR TFCS#3 4.1.1.3.2.2	DL_AMR#3	DAMR37s0	DAMR38s0 DAMR38s8 DAMR38s9	-	-	-	-	-	-	-	-	-
		TR25.944 4.1.1.3.1.1 DCCH 4.1.1.3.1.6 ISDN 4.1.1.3.2.5	DL_ISDN	DISDN7s0	DISDN8s0 DISDN8s8 DISDN8s9	-	-	-	-	-	-	-	-	-
-	TS25.104 Annex A.1	TR25.944 4.1.2.2.1.1 DCCH 4.1.2.2.1.2 AMR TFCS#1 4.1.2.2.2.2	UL_AMR#1	-	-	-	-	-	-	-	-	-	-	-
		TR25.944 4.1.2.2.1.1 DCCH 4.1.2.2.1.2 AMR TFCS#2 4.1.2.2.2.2	UL_AMR#2	-	-	-	-	-	-	-	-	-	-	-
		TR25.944 4.1.2.2.1.1 DCCH 4.1.2.2.1.2 AMR TFCS#3 4.1.2.2.2.2	UL_AMR#3	-	-	-	-	-	-	-	-	-	-	-
		TR25.944 4.1.2.2.1.1 DCCH 4.1.2.2.1.6 ISDN 4.1.2.2.2.2	UL_ISDN	-	-	-	-	-	-	-	-	-	-	-

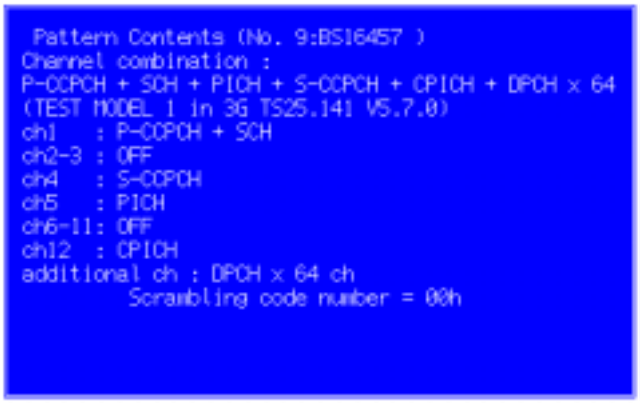
BS11657



BS13257

* Scrambling code number = 10 HEX: BS116571 BS132571
* Scrambling code number = 20 HEX: BS116572 BS132572
* Scrambling code number = 30 HEX: BS116573 BS132573

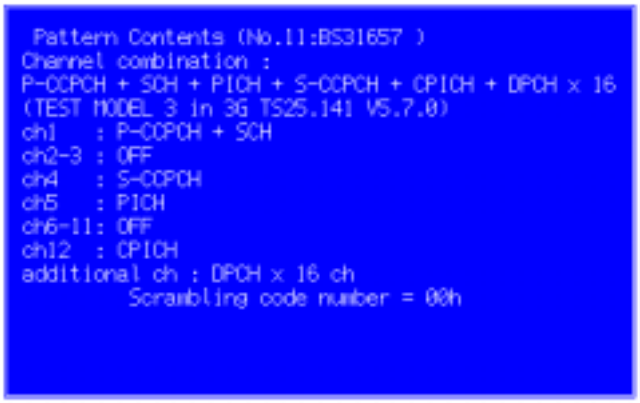
BS16457



BS257

* Scrambling code number = 10 HEX: BS164571 BS2571
* Scrambling code number = 20 HEX: BS164572 BS2572
* Scrambling code number = 30 HEX: BS164573 BS2573

BS31657

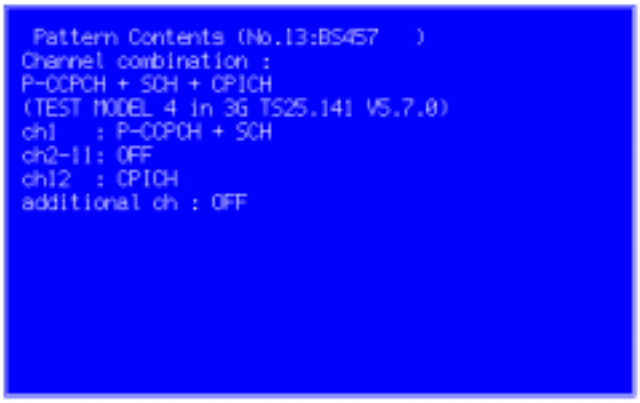


BS33257

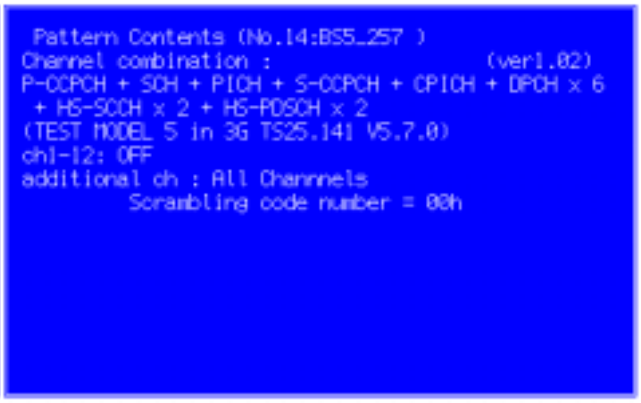
* Scrambling code number = 10 HEX: BS316571 BS332571
* Scrambling code number = 20 HEX: BS316572 BS332572
* Scrambling code number = 30 HEX: BS316573 BS332573

* P-CCPCH: Conformity to TS25.141 6.1.1.6.1 P-CCPCH
* PICH: Conformity to TS25.141 6.1.1.6.2 PICH

BS457

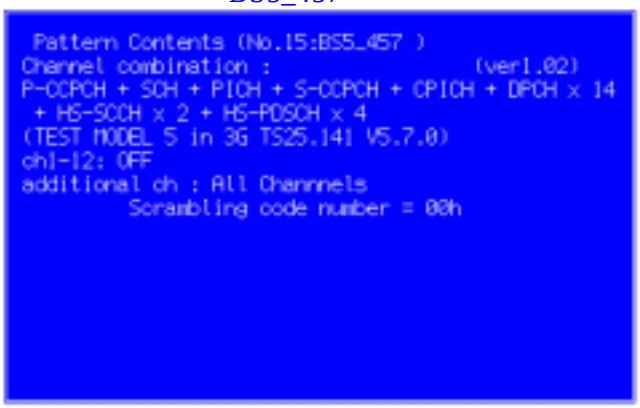


BS5_257

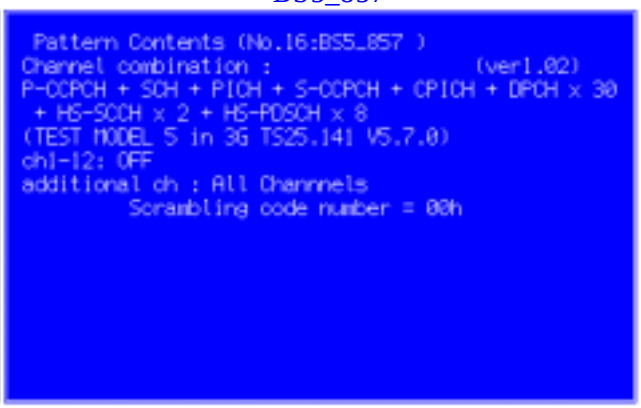


* Scrambling code number = 10 HEX: BS4571 BS5_2571
* Scrambling code number = 20 HEX: BS4572 BS5_2572
* Scrambling code number = 30 HEX: BS4573 BS5_2573

BS5_457



BS5_857



* Scrambling code number = 10 HEX: BS5_4571 BS5_8571
* Scrambling code number = 20 HEX: BS5_4572 BS5_8572
* Scrambling code number = 30 HEX: BS5_4573 BS5_8573

B11657d

B13257d

* Scrambling code number = 20,30 HEX: BS11657d2 BS13257d2

* P-CCPCH: Conformity to TS25.141 6.1.1.6.1 P-CCPCH
* PICH: Conformity to TS25.141 6.1.1.6.2 PICH

B16457d

B5_257d

* Scrambling code number = 20,30 HEX: BS16457d2 B5_257d2

B5_457d

B5_857d

* Scrambling code number = 20,30 HEX: B5_457d2 B5_857d2

* P-CCPCH: Conformity to TS25.141 6.1.1.6.1 P-CCPCH
* PICH: Conformity to TS25.141 6.1.1.6.2 PICH

DL_C31

```
 Pattern Contents (No.18:DL_C31   )
Channel combination : TS25.101 V3.6.0 Annex C3.1
for Reference sensitivity level

ch1    : P-CCPCH + P/S-SCH
ch2-3  : OFF
ch4    : DL-DPCH (30 ksps)
         3G TS25.211 V3.6.0, TS25.212,213 V3.5.0
         3G TS25.101 V3.6.0 Annex A.3.1
         DL reference measurement channel 12.2 kbps
ch5    : PICH
ch6    : CPICH
ch7-12: OFF
Add ch: OFF
```

D32T18s0

```
 Pattern Contents (No. 5:D32T18s0)
Channel combination : TS25.101 V3.8.0 Annex C3.2
for Performance requirement
Demodulation of DCH TEST1 (BLER=10^-2)

ch1    : P-CCPCH + P/S-SCH
ch4    : DL-DPCH (30 ksps)
         3G TS25.211 V3.7.0, TS25.212,213 v3.6.0
         3G TS25.101 v3.7.0 Annex A.3.1
         DL reference measurement channel 12.2 kbps
ch5    : PICH
ch6    : CPICH
ch2-3, 7-12: OFF
Add ch: OCNS (TS25.101 V3.8.0 table C.6)
         Scrambling code number = 00h
```

D32T28s0

```
 Pattern Contents (No. 6:D32T28s0)
Channel combination : TS25.101 V3.8.0 Annex C3.2
for Performance requirement
Demodulation of DCH TEST2 (BLER=10^-2)

ch1    : P-CCPCH + P/S-SCH
ch4    : DL-DPCH (120 ksps)
         3G TS25.211 V3.7.0, TS25.212,213 V3.6.0
         3G TS25.101 V3.7.0 Annex A.3.2
         DL reference measurement channel 64 kbps
ch5    : PICH
ch6    : CPICH
ch2-3, ch7-12: OFF
Add ch: OCNS (TS25.101 V3.8.0 table C.6)
         Scrambling code number = 00h
```

* Scrambling code number = 80 HEX: D32T18s8 D32T28s8
* Scrambling code number = 90 HEX: D32T18s9 D32T28s9

D32T38s0

```
 Pattern Contents (No. 7:D32T38s0)
Channel combination : TS25.101 V3.8.0 Annex C3.2
for Performance requirement
Demodulation of DCH TEST3 (BLER=10^-2)

ch1    : P-CCPCH + P/S-SCH
ch4    : DL-DPCH (240 ksps)
         3G TS25.211 V3.7.0, TS25.212,213 V3.6.0
         3G TS25.101 V3.7.0 Annex A.3.3
         DL reference measurement channel 144 kbps
ch5    : PICH
ch6    : CPICH
ch2-3, ch7-12: OFF
Add ch: OCNS (TS25.101 V3.8.0 table C.6)
         Scrambling code number = 00h
```

D32T48s0

```
 Pattern Contents (No. 8:D32T48s0)
Channel combination : TS25.101 V3.8.0 Annex C3.2
for Performance requirement
Demodulation of DCH TEST4 (BLER=10^-2)

ch1    : P-CCPCH + P/S-SCH
ch4    : DL-DPCH (480 ksps)
         3G TS25.211 V3.7.0, TS25.212,213 v3.6.0
         3G TS25.101 v3.7.0 Annex A.3.4
         DL reference measurement channel 384 kbps
ch5    : PICH
ch6    : CPICH
ch2-3, ch7-12: OFF
Add ch: OCNS (TS25.101 V3.8.0 table C.6)
         Scrambling code number = 00h
```

* Scrambling code number = 80 HEX: D32T38s8 D32T48s8
* Scrambling code number = 90 HEX: D32T38s9 D32T48s9

* P-CCPCH: 11 bits SFN is mapped on BCH.
* DL_DPCH: DPCH_Ec/Ior conforms to BLER=10^{-2} of “Demodulation of DCH”.
* PICH: Conformity to TS25.141 6.1.1.6.2 PICH
* OCNS: PN9 that added offset to initial value in each code
  OCNS of one frame cycle which reset PN9 in each frame

DL_INTR

```
 Pattern Contents (No.19:DL_INTR )
Channel combination : TS25.101 V3.8.0 Annex C.4
for Modulation Interfere
ch1    : P-CCPCH + SCH
ch2-3  : OFF
ch4    : OFF
ch5    : PICH
ch6-11: OFF
ch12   : CPICH
additional ch : OCNS (TS25.101 V3.8.0 table C.6)
         Scrambling code number = 2000h
```

* SCH: S-SCH_SSC = Group 0

4Ps0

```
 Pattern Contents (No. 1:4Ps0    )
Channel combination : TS25.101 V5.4.0 Annex C3.5
for Performance requirement Demodulation of DCH
in multi-path fading propagation condition TEST21

ch1    : P-CCPCH + P/S-SCH
ch4    : DPCH (30 ksps)
         3G TS25.141 V5.4.0 Annex A.4A
         DL RMC using DPCCH with 4 pilot bits
         as phase reference
ch5    : PICH
ch6    : CPICH
ch2-3, ch7-12: OFF
Add ch: OCNS (TS25.101 V5.4.0 table C.6)
         Scrambling code number = 00h
```

* Scrambling code number = 80 HEX: 4Ps8
* Scrambling code number = 90 HEX: 4Ps9

OTD1s0

```
 Pattern Contents (No.20:OTD1s0  )
Channel combination : TS25.101 V5.4.0 Annex C3.3
for Performance requirement Demodulation of DCH in
open-loop transmit diversity mode TEST1 antenna 1
ch1    : P-CCPCH + P/S-SCH (TSTD)
ch2    : CPICH
ch4    : DL-DPCH (30 ksps)
         3G TS25.101 V5.4.0 Annex A.3.1
         DL reference measurement channel 12.2 kbps
ch5    : PICH
ch3, 6-12: OFF
Add ch: OCNS (TS25.101 V5.4.0 table C.6)
         Scrambling code number = 00h
```

OTD2s0

```
 Pattern Contents (No.21:OTD2s0  )
Channel combination : TS25.101 V5.4.0 Annex C3.3
for Performance requirement Demodulation of DCH in
open-loop transmit diversity mode TEST1 antenna 2

ch1    : P-CCPCH + P/S-SCH (TSTD)
ch2    : CPICH
ch4    : DL-DPCH (30 ksps)
         3G TS25.101 V5.4.0 Annex A.3.1
         DL reference measurement channel 12.2 kbps
ch5    : PICH
ch3, 6-12: OFF
Add ch: OCNS (TS25.101 V5.4.0 table C.6)
         Scrambling code number = 00h
```

* Scrambling code number = 80 HEX: OTD1s8 OTD2s8
* Scrambling code number = 90 HEX: OTD1s9 OTD2s9
* OCNS: Not STTD encoding

* P-CCPCH: 11 bits SFN is mapped on BCH.
* PICH: Conformity to TS25.141 6.1.1.6.2 PICH
* OCNS: PN9 that added offset to initial value in each code
  OCNS of one frame cycle which reset PN9 in each frame

DCP11540

```
 Pattern Contents (No.12:DCP11540)
Channel combination : TS25.101 V5.4.0 Annex C3.2
for Performance requirement
Downlink compressed mode Test1

ch1    : P-CCPCH + P/S-SCH
ch4    : DL-DPCH (Normal frame)
ch5    : DL-DPCH (Compressed frame)
         TS25.101 V5.4.0 Annex A.3.1
         DL reference measurement channel 12.2 kbps
         TS25.101 V5.4.0 Annex A.5 pattern 1 set 1
ch6    : CPICH
ch12   : PICH (32bit 'FFFF0000' repeat)
ch2-3, 7-11: OFF
Add ch: OCNS (TS25.101 V5.4.0 table C.6)
```

DCP12540

```
 Pattern Contents (No.13:DCP12540)
Channel combination : TS25.101 V5.4.0 Annex C3.2
for Performance requirement
Downlink compressed mode Test1

ch1    : P-CCPCH + P/S-SCH
ch4    : DL-DPCH (Normal frame)
ch5    : DL-DPCH (Compressed frame)
         TS25.101 V5.4.0 Annex A.3.1
         DL reference measurement channel 12.2 kbps
         TS25.101 V5.4.0 Annex A.5 pattern 1 set 2
ch6    : CPICH
ch12   : PICH (32bit 'FFFF0000' repeat)
ch2-3, 7-11: OFF
Add ch: OCNS (TS25.101 V5.4.0 table C.6)
```

* Scrambling code number = 80 HEX: DCP11548 DCP12548
* Scrambling code number = 90 HEX: DCP11549 DCP12549

DCP21540

```
 Pattern Contents (No.14:DCP21540)
Channel combination : TS25.101 V5.4.0 Annex C3.2
for Performance requirement
Downlink compressed mode Test1

ch1    : P-CCPCH + P/S-SCH
ch4    : DL-DPCH (Normal frame)
ch5    : DL-DPCH (Compressed frame)
         TS25.101 V5.4.0 Annex A.3.1
         DL reference measurement channel 12.2 kbps
         TS25.101 V5.4.0 Annex A.5 pattern 2 set 1
ch6    : CPICH
ch12   : PICH (32bit 'FFFF0000' repeat)
ch2-3, 7-11: OFF
Add ch: OCNS (TS25.101 V5.4.0 table C.6)
```

DCP22540

```
 Pattern Contents (No.15:DCP22540)
Channel combination : TS25.101 V5.4.0 Annex C3.2
for Performance requirement
Downlink compressed mode Test1

ch1    : P-CCPCH + P/S-SCH
ch4    : DL-DPCH (Normal frame)
ch5    : DL-DPCH (Compressed frame)
         TS25.101 V5.4.0 Annex A.3.1
         DL reference measurement channel 12.2 kbps
         TS25.101 V5.4.0 Annex A.5 pattern 2 set 2
ch6    : CPICH
ch12   : PICH (32bit 'FFFF0000' repeat)
ch2-3, 7-11: OFF
Add ch: OCNS (TS25.101 V5.4.0 table C.6)
```

* Scrambling code number = 80 HEX: DCP21548 DCP22548
* Scrambling code number = 90 HEX: DCP21549 DCP22549

DCP23540

```
 Pattern Contents (No.16:DCP23540)
Channel combination : TS25.101 V5.4.0 Annex C3.2
for Performance requirement
Downlink compressed mode Test1

ch1    : P-CCPCH + P/S-SCH
ch4    : DL-DPCH (Normal frame)
ch5    : DL-DPCH (Compressed frame)
         TS25.101 V5.4.0 Annex A.3.1
         DL reference measurement channel 12.2 kbps
         TS25.101 V5.4.0 Annex A.5 pattern 2 set 3
ch6    : CPICH
ch12   : PICH (32bit 'FFFF0000' repeat)
ch2-3, 7-11: OFF
Add ch: OCNS (TS25.101 V5.4.0 table C.6)
```

* Scrambling code number = 80 HEX: DCP23548
* Scrambling code number = 90 HEX: DCP23549

* P-CCPCH: 11 bits SFN is mapped on BCH.
* OCNS: PN9 that added offset to initial value in each code
  OCNS of one frame cycle which reset PN9 in each frame

BTFD1s0

```
 Pattern Contents (No. 2:BTFD1s0 )
Channel combination : TS25.101 V5.4.0 Annex C3.2
for Performance requirement
Blind transport format detection (rate 1)

ch1    : P-CCPCH + P/S-SCH
ch4    : DL-DPCH (30 ksps)
         3G TS25.101 v5.4.0 Annex A.4
         DL reference measurement channel
                          for BTFD 12.2 kbps
ch5    : PICH
ch6    : CPICH
ch2-3, 7-12: OFF
Add ch: OCNS (TS25.101 V5.4.0 table C.6)
         Scrambling code number = 00h
```

BTFD2s0

```
 Pattern Contents (No. 3:BTFD2s0 )
Channel combination : TS25.101 V5.4.0 Annex C3.2
for Performance requirement
Blind transport format detection (rate 2)

ch1    : P-CCPCH + P/S-SCH
ch4    : DL-DPCH (30 ksps)
         3G TS25.101 v5.4.0 Annex A.4
         DL reference measurement channel
                          for BTFD 7.95 kbps
ch5    : PICH
ch6    : CPICH
ch2-3, 7-12: OFF
Add ch: OCNS (TS25.101 V5.4.0 table C.6)
         Scrambling code number = 00h
```

* Scrambling code number = 80 HEX: BTFD1s8 BTFD2s8
* Scrambling code number = 90 HEX: BTFD1s9 BTFD2s9

BTFD3s0

```
 Pattern Contents (No. 4:BTFD3s0 )
Channel combination : TS25.101 V5.4.0 Annex C3.2
for Performance requirement
Blind transport format detection (rate 3)

ch1    : P-CCPCH + P/S-SCH
ch4    : DL-DPCH (30 ksps)
         3G TS25.101 v5.4.0 Annex A.4
         DL reference measurement channel
                          for BTFD 1.95 kbps
ch5    : PICH
ch6    : CPICH
ch2-3, 7-12: OFF
Add ch: OCNS (TS25.101 V5.4.0 table C.6)
         Scrambling code number = 00h
```

PCHs0

```
 Pattern Contents (No.22:PCHs0    )
Channel combination : TS25.101 V5.4.0 Annex C3.2
for Performance requirement
Demodulation of PCH TEST1
ch1    : P-CCPCH + P/S-SCH
ch4    : PCH (30 ksps)
         3G TS25.101 V5.4.0 Annex A.6
         DL reference measurement channel for PCH
ch5    : PICH
ch6    : CPICH
ch2-3, ch7-12: OFF
Add ch: OCNS (TS25.101 V5.4.0 table C.6)
         Scrambling code number = 00h
```

* Scrambling code number = 80 HEX: BTFD3s8 PCHs8
* Scrambling code number = 90 HEX: BTFD3s9 PCHs9

* P-CCPCH: 11 bits SFN is mapped on BCH.
* PICH: Conformity to TS25.141 6.1.1.6.2 PICH
* OCNS: PN9 that added offset to initial value in each code
  OCNS of one frame cycle which reset PN9 in each frame

F1P0s0

```
 Pattern Contents (No.21:F1P0s0  )
Channel combination : TS25.101 V5.7.0 Annex C5.1
for Performance requirement HSDPA (ver. 1.00)

ch1   : P-CCPCH + P/S-SCH
ch2   : CPICH
ch4   : DL-DPCH (RMC 12.2kbps)
ch5   : HS-SCCH (Xccs=40h(P=5,O=1),Xms=0,Xtbs=29h,
                 Xhap=0to5,Xrv=0,Xnd=0/1,Xue=0)
ch6-11: OCNS (PN9)
ch12  : PICH (Dummy)
Add ch: HS-PDSCH (FRC QPSK H-Set1 RV=0)
             Channelization Code = 1,2,3,4,5)
         Scrambling code number = 00h
```

F1A0s0

```
 Pattern Contents (No.20:F1A0s0  )
Channel combination : TS25.101 V5.7.0 Annex C5.1
for Performance requirement HSDPA (ver. 1.00)

ch1   : P-CCPCH + P/S-SCH
ch2   : CPICH
ch4   : DL-DPCH (RMC 12.2kbps)
ch5   : HS-SCCH (Xccs=30h(P=4,O=1),Xms=1,Xtbs=24h,
                 Xhap=0to5,Xrv=0,Xnd=0/1,Xue=0)
ch6-11: OCNS (PN9)
ch12  : PICH (Dummy)
Add ch: HS-PDSCH (FRC 16QAM H-Set1 RV=0
             Channelization Code = 1,2,3,4)
         Scrambling code number = 00h
```

F2P0s0

```
 Pattern Contents (No.23:F2P0s0  )
Channel combination : TS25.101 V5.7.0 Annex C5.1
for Performance requirement HSDPA (ver. 1.00)

ch1   : P-CCPCH + P/S-SCH
ch2   : CPICH
ch4   : DL-DPCH (RMC 12.2kbps)
ch5   : HS-SCCH (Xccs=40h(P=5,O=1),Xms=0,Xtbs=29h,
                 Xhap=0to5,Xrv=0,Xnd=0/1,Xue=0)
ch6-11: OCNS (PN9)
ch12  : PICH (Dummy)
Add ch: HS-PDSCH (FRC QPSK H-Set2 RV=0)
             Channelization Code = 1,2,3,4,5)
         Scrambling code number = 00h
```

F2A0s0

```
 Pattern Contents (No.22:F2A0s0  )
Channel combination : TS25.101 V5.7.0 Annex C5.1
for Performance requirement HSDPA (ver. 1.00)

ch1   : P-CCPCH + P/S-SCH
ch2   : CPICH
ch4   : DL-DPCH (RMC 12.2kbps)
ch5   : HS-SCCH (Xccs=30h(P=4,O=1),Xms=1,Xtbs=24h,
                 Xhap=0to5,Xrv=0,Xnd=0/1,Xue=0)
ch6-11: OCNS (PN9)
ch12  : PICH (Dummy)
Add ch: HS-PDSCH (FRC 16QAM H-Set2 RV=0
             Channelization Code = 1,2,3,4)
         Scrambling code number = 00h
```

F3P0s0

```
 Pattern Contents (No.25:F3P0s0  )
Channel combination : TS25.101 V5.7.0 Annex C5.1
for Performance requirement HSDPA (ver. 1.00)

ch1   : P-CCPCH + P/S-SCH
ch2   : CPICH
ch4   : DL-DPCH (RMC 12.2kbps)
ch5   : HS-SCCH (Xccs=40h(P=5,O=1),Xms=0,Xtbs=29h,
                 Xhap=0to5,Xrv=0,Xnd=0/1,Xue=0)
ch6-11: OCNS (PN9)
ch12  : PICH (Dummy)
Add ch: HS-PDSCH (FRC QPSK H-Set3 RV=0)
             Channelization Code = 1,2,3,4,5)
         Scrambling code number = 00h
```

F3A0s0

```
 Pattern Contents (No.24:F3A0s0  )
Channel combination : TS25.101 V5.7.0 Annex C5.1
for Performance requirement HSDPA (ver. 1.00)

ch1   : P-CCPCH + P/S-SCH
ch2   : CPICH
ch4   : DL-DPCH (RMC 12.2kbps)
ch5   : HS-SCCH (Xccs=30h(P=4,O=1),Xms=1,Xtbs=24h,
                 Xhap=0to5,Xrv=0,Xnd=0/1,Xue=0)
ch6-11: OCNS (PN9)
ch12  : PICH (Dummy)
Add ch: HS-PDSCH (FRC 16QAM H-Set3 RV=0
             Channelization Code = 1,2,3,4)
         Scrambling code number = 00h
```

F4P0s0

```
 Pattern Contents (No.26:F4P0s0  )
Channel combination : TS25.101 V5.7.0 Annex C5.1
for Performance requirement HSDPA (ver. 1.01)

ch1   : P-CCPCH + P/S-SCH
ch2   : CPICH
ch4   : DL-DPCH (RMC 12.2kbps)
ch5   : HS-SCCH (Xccs=40h(P=5,O=1),Xms=0,Xtbs=29h,
                 Xhap=0to5,Xrv=0,Xnd=0/1,Xue=0)
ch6-11: OCNS (PN9)
ch12  : PICH (Dummy)
Add ch: HS-PDSCH (FRC QPSK H-Set4 RV=0)
             Channelization Code = 1,2,3,4,5)
         Scrambling code number = 00h
```

F5P0s0

```
 Pattern Contents (No.27:F5P0s0  )
Channel combination : TS25.101 V5.7.0 Annex C5.1
for Performance requirement HSDPA (ver. 1.00)

ch1   : P-CCPCH + P/S-SCH
ch2   : CPICH
ch4   : DL-DPCH (RMC 12.2kbps)
ch5   : HS-SCCH (Xccs=40h(P=5,O=1),Xms=0,Xtbs=29h,
                 Xhap=0to5,Xrv=0,Xnd=0/1,Xue=0)
ch6-11: OCNS (PN9)
ch12  : PICH (Dummy)
Add ch: HS-PDSCH (FRC QPSK H-Set5 RV=0)
             Channelization Code = 1,2,3,4,5)
         Scrambling code number = 00h
```

* P-CCPCH: 11 bits SFN is mapped on BCH.
* DL_DPCH: DPCH_Ec/Ior conforms to BLER=10^{-2} of “Demodulation of DCH”.
* PICH: Conformity to TS25.141 6.1.1.6.2 PICH

* HS-SCCH:

H-Set 1 [H]	Q	P	S	K									16 - Q A M											
Channelization-code-set information (xccs)	40			40			40			40			30			30			30			30		
Modulation scheme information (xms)	0			0			0			0			1			1			1			1		
Transport-block size information (xtbs)	29			29			29			29			24			24			24			24		
Hybrid-ARQ process information (xhap)	0			1			0			1			0			1			0			1		
Redundancy and constellation version (xrv)	0			0			0			0			0			0			0			0		
New data indicator (xnd)	0			0			1			1			0			0			1			1		
UE identity (xue)	0			0			0			0			0			0			0			0		

H-Set 2 [H]	Q	P	S	K									16 - Q A M											
Channelization-code-set information (xccs)	40		40		40		40		40		40		30		30		30		30		30		30	
Modulation scheme information (xms)	0		0		0		0		0		0		1		1		1		1		1		1	
Transport-block size information (xtbs)	29		29		29		29		29		29		24		24		24		24		24		24	
Hybrid-ARQ process information (xhap)	0		1		2		0		1		2		0		1		2		0		1		2	
Redundancy and constellation version (xrv)	0		0		0		0		0		0		0		0		0		0		0		0	
New data indicator (xnd)	0		0		0		1		1		1		0		0		0		1		1		1	
UE identity (xue)	0		0		0		0		0		0		0		0		0		0		0		0	

H-Set 3 [H]	Q	P	S	K									16 - Q A M											
Channelization-code-set information (xccs)	40	40	40	40	40	40	40	40	40	40	40	40	30	30	30	30	30	30	30	30	30	30	30	30
Modulation scheme information (xms)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
Transport-block size information (xtbs)	29	29	29	29	29	29	29	29	29	29	29	29	24	24	24	24	24	24	24	24	24	24	24	24
Hybrid-ARQ process information (xhap)	0	1	2	3	4	5	0	1	2	3	4	5	0	1	2	3	4	5	0	1	2	3	4	5
Redundancy and constellation version (xrv)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
New data indicator (xnd)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
UE identity (xue)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

H-Set 4 [H]	Q	P	S	K								
Channelization-code-set information (xccs)			40		40				40		40	
Modulation scheme information (xms)			0		0				0		0	
Transport-block size information (xtbs)			29		29				29		29	
Hybrid-ARQ process information (xhap)			0		1				0		1	
Redundancy and constellation version (xrv)			0		0				0		0	
New data indicator (xnd)			0		0				1		1	
UE identity (xue)			0		0				0		0	

H-Set 5 [H]	Q	P	S	K								
Channelization-code-set information (xccs)			40	40	40				40	40	40	
Modulation scheme information (xms)			0	0	0				0	0	0	
Transport-block size information (xtbs)			29	29	29				29	29	29	
Hybrid-ARQ process information (xhap)			0	1	2				0	1	2	
Redundancy and constellation version (xrv)			0	0	0				0	0	0	
New data indicator (xnd)			0	0	0				1	1	1	
UE identity (xue)			0	0	0				0	0	0	

* DTX (Generating off)

* 12 supframe (12 TTI) repeat •----------→ •----------→

* HS-PDSCH: HS-PDSCH of 6 frames (30 subframes) cycle which reset PN15 in each TTI of HS-DSCH

DAMR18s0

DAMR28s0

* Scrambling code number = 80 HEX: DAMR18s8 DAMR28s8
* Scrambling code number = 90 HEX: DAMR18s9 DAMR28s9

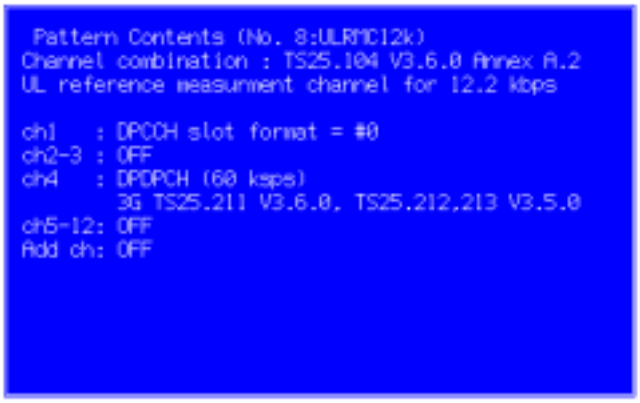
DAMR38s0

DISDN8s0

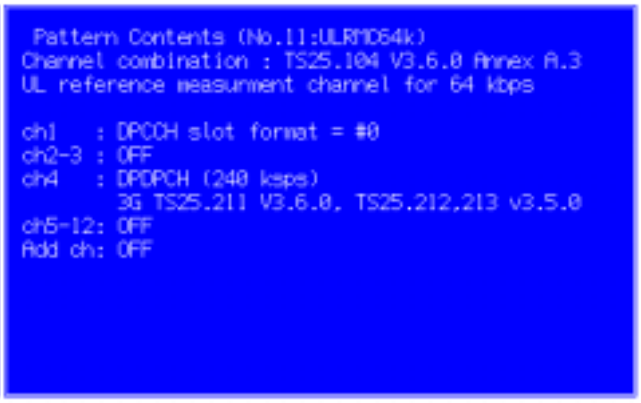
* Scrambling code number = 80 HEX: DAMR38s8 DISDN8s8
* Scrambling code number = 90 HEX: DAMR38s9 DISDN8s9

* P-CCPCH: 11 bits SFN is mapped on BCH.
* DL_DPCH: DPCH_Ec/Ior conforms to BLER=10^{-2} of “Demodulation of DCH”.
* PICH: Conformity to TS25.141 6.1.1.6.2 PICH
* OCNS: PN9 that added offset to initial value in each code
OCNS of one frame cycle which reset PN9 in each frame

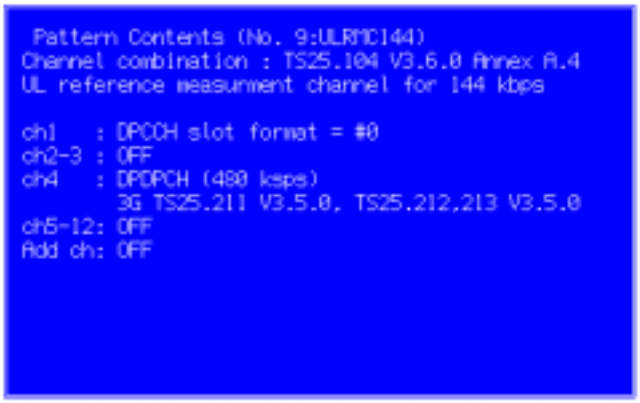
ULRMC12k



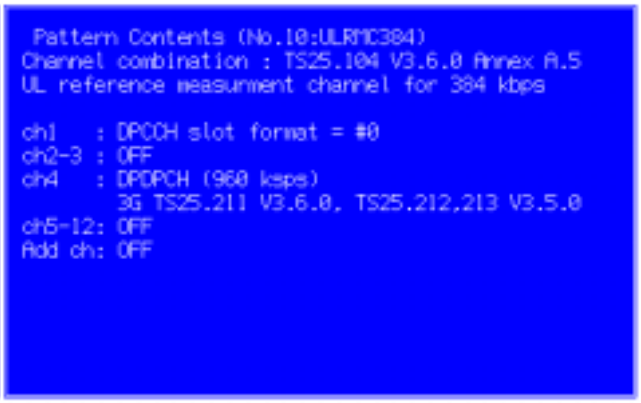
ULRMC64k



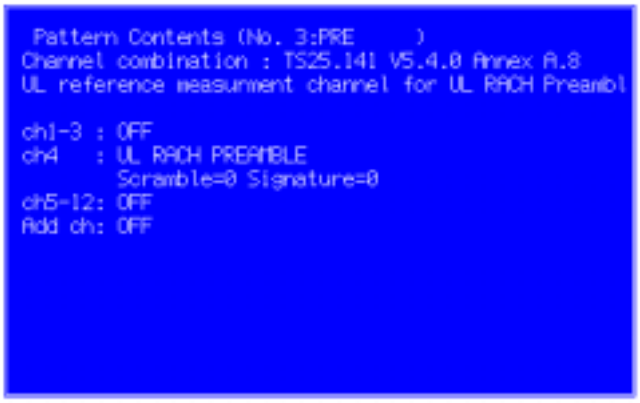
ULRMC144



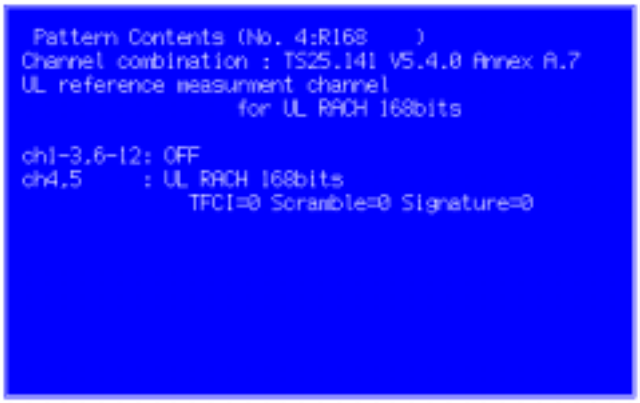
ULRMC384



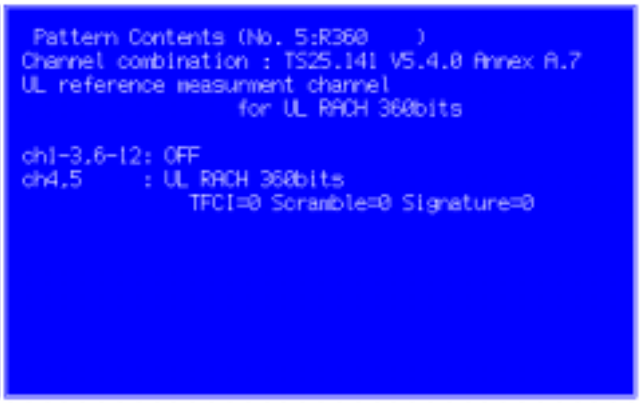
PRE



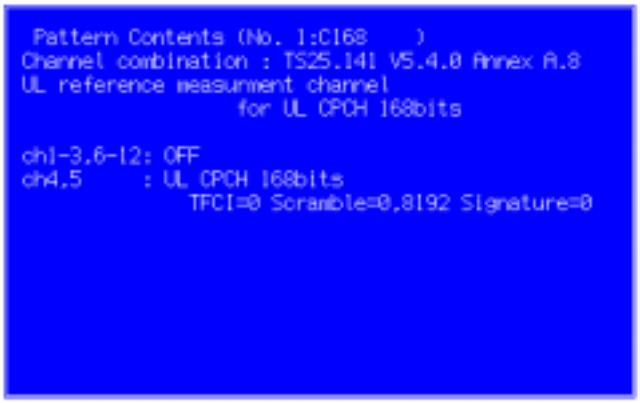
R168



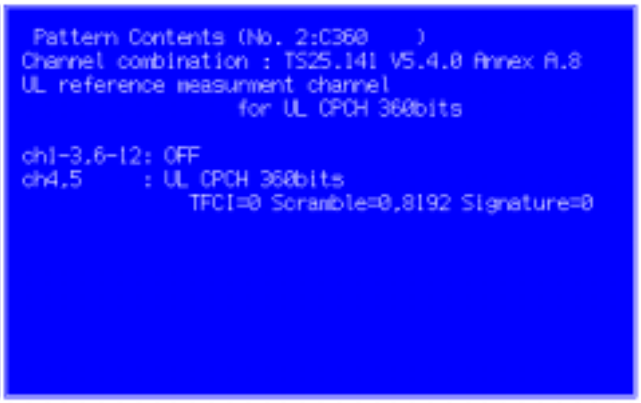
R360



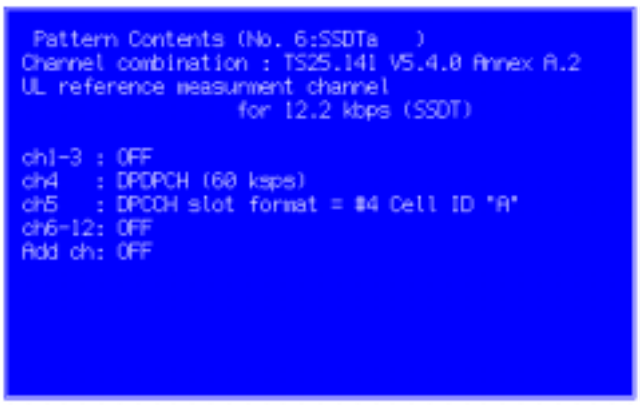
C168



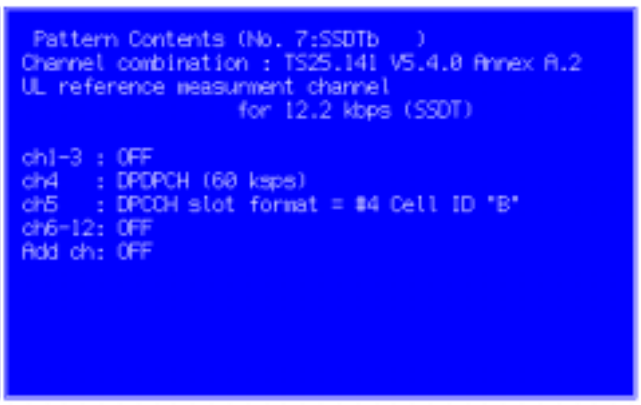
C360



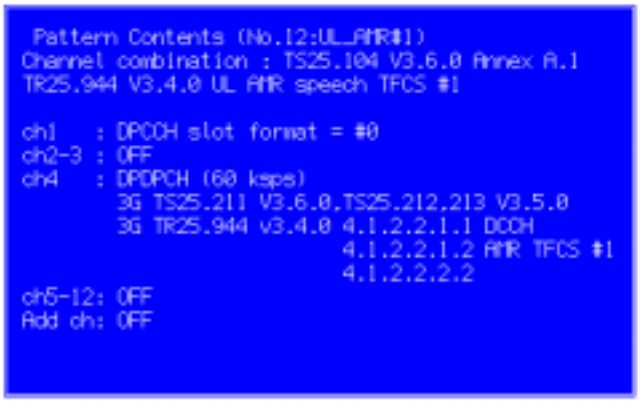
SSDTa



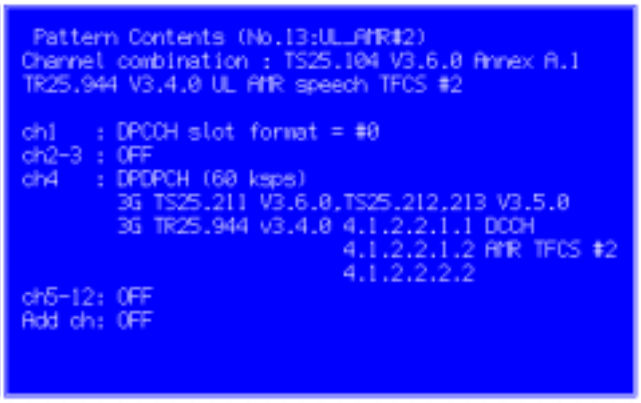
SSDTb



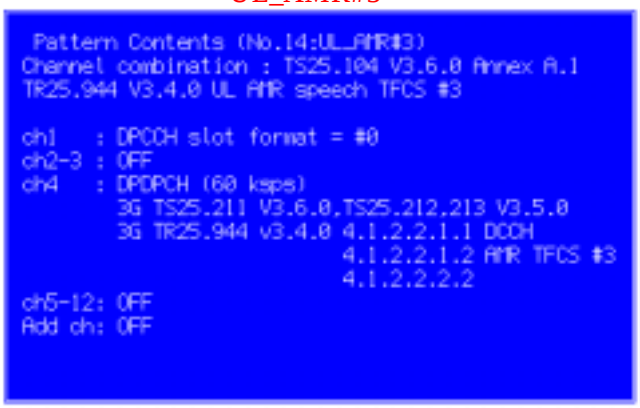
UL_AMR#1



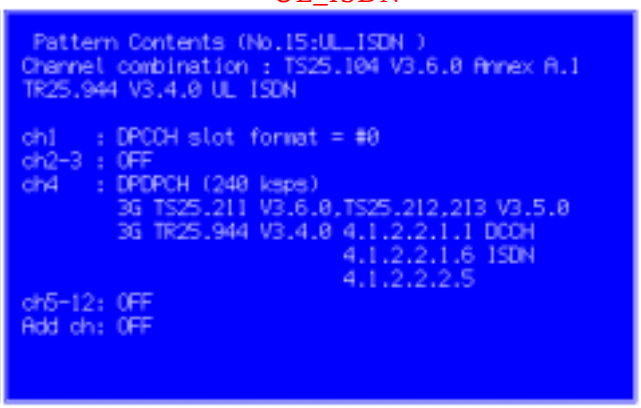
UL_AMR#2



UL_AMR#3



UL_ISDN



Anritsu

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ANRITSU CORPORATION
1800 Onna, Atsugi-shi, Kanagawa, 243-8555 Japan
Phone: +81-46-223-1111
Fax: +81-46-296-1264

● **U.S.A.**
ANRITSU COMPANY
TX OFFICE SALES AND SERVICE
1155 East Collins Blvd., Richardson, TX 75081, U.S.A.
Toll Free: 1-800-ANRITSU (267-4878)
Phone: +1-972-644-1777
Fax: +1-972-644-3416

● **Canada**
ANRITSU ELECTRONICS LTD.
700 Silver Seven Road, Suite 120, Kanata,
ON K2V 1C3, Canada
Phone: +1-613-591-2003
Fax: +1-613-591-1006

● **Brasil**
ANRITSU ELETRÔNICA LTDA.
Praca Amadeu Amaral, 27 - 1 andar
01327-010 - Paraiso, Sao Paulo, Brazil
Phone: +55-11-3283-2511
Fax: +55-11-3886940

● **U.K.**
ANRITSU LTD.
200 Capability Green, Luton, Bedfordshire LU1 3LU, U.K.
Phone: +44-1582-433280
Fax: +44-1582-731303

● **Germany**
ANRITSU GmbH
Grafenberger Allee 54-56, 40237 Düsseldorf, Germany
Phone: +49-211-96855-0
Fax: +49-211-96855-55

● **France**
ANRITSU S.A.
9, Avenue du Québec Z.A. de Courtabœuf 91951 Les Ulis Cedex, France
Phone: +33-1-60-92-15-50
Fax: +33-1-64-46-10-65

● **Italy**
ANRITSU S.p.A.
Via Elio Vittorini, 129, 00144 Roma EUR, Italy
Phone: +39-06-509-9711
Fax: +39-06-502-2425

● **Sweden**
ANRITSU AB
Borgafjordsgatan 13 164 40 Kista, Sweden
Phone: +46-853470700
Fax: +46-853470730

● **Denmark**
Anritsu AB Danmark
Korskildelund 6 DK - 2670 Greve, Denmark
Phone: +45-36915035
Fax: +45-43909371

● **Singapore**
ANRITSU PTE LTD.
10, Hoe Chiang Road #07-01/02, Keppel Towers,
Singapore 089315
Phone: +65-6282-2400
Fax: +65-6282-2533

● **Hong Kong**
ANRITSU COMPANY LTD.
Suite 923, 9/F., Chinachem Golden Plaza, 77 Mody Road, Tsimshatsui East, Kowloon, Hong Kong, China
Phone: +852-2301-4980
Fax: +852-2301-3545

● **P. R. China**
ANRITSU COMPANY LTD.
Beijing Representative Office
Room 1515, Beijing Fortune Building, No. 5 North Road, the East 3rd Ring Road, Chao-Yang District
Beijing 100004, P.R. China
Phone: +86-10-6590-9230

● **Korea**
ANRITSU CORPORATION
8F Hyun Juk Bldg. 832-41, Yeoksam-dong,
Kangnam-ku, Seoul, 135-080, Korea
Phone: +82-2-553-6603
Fax: +82-2-553-6604

● **Australia**
ANRITSU PTY LTD.
Unit 3/170 Forster Road Mt. Waverley, Victoria, 3149, Australia
Phone: +61-3-9558-8177
Fax: +61-3-9558-8255

● **Taiwan**
ANRITSU COMPANY INC.
7F, No. 316, Sec. 1, NeiHu Rd., Taipei, Taiwan
Phone: +886-2-8751-1816
Fax: +886-2-8751-1817

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