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PRODUCT INTRODUCTION

MX368041B

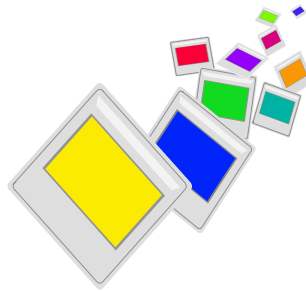
W-CDMA Software

ANRITSU CORPORATION

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MX368041B W-CDMA Software for MG3681A Digital Modulation Signal Generator



2002. 8

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Scope

- **MX368041B is no export license for the convenience of export by revising MX368041A to MX368041B which made the setting except 3GPP standard impossible.**
- **This document introduces the difference with MX368041A and MX368041B.**
 - » Scrambling Code Edit screen

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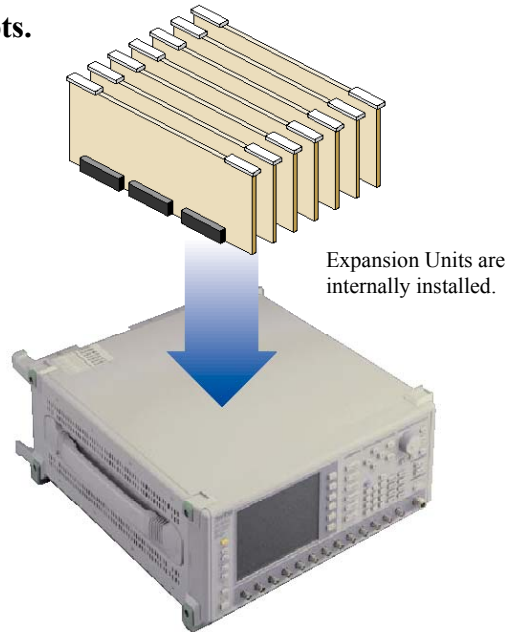
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The Platform with Excellent Expandability

Expansion Units is installable up to seven slots.

Expansion unit	Software
MU368010A TDMA Modulation Unit	MX368011A PDC Software MX368012A GSM Device Test Software
MU368040A CDMA Modulation Unit	MX368041B W-CDMA Software MX368042A IS-95 Device Test Software
MU368030A Universal Modulation Unit	MX368031A Device Test Signal Generation Software MX368033A CDMA2000 1xEV-DO Signal Generation Software MX368034A PDC Packet Software
MU368060A AWGN Unit	



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Expansion Unit and Software

Communication system	Software	CompactFlash™	Expansion units
W-CDMA(3GPP)	MX368041B W-CDMA Software		 MU368040A CDMA Modulation Unit
cdmaOne	MX368042A IS-95 Device Test Software		
PDC	MX368011A PDC Software		 MU368010A TDMA Modulation Unit
GSM	MX368012A GSM Device Test Software		
CDMA2000 1x CDMA2000 1xEV-DO ¹ EDGE ² , GSM ² , PDC ² , NADC ² , PHS ²	MX368031A ³ Device Test Signal Generation Software		 MU368030A Universal Modulation Unit
CDMA2000 1xEV-DO	MX368033A ³ CDMA2000 1xEV-DO Signal Generation Software		
PDC Packet	MX368034A ³ PDC Packet Software		

*1: Only 16QAM modulation is available in Forward, 8PSK and QPSK modulations are not available.

Neither Forward or Reverse can be used for receiver test as coding format is not performed.

*2: Continuous modulation signal based on the communication system.

*3: The signal format to output is selectable by downloading the waveform data included in the software from a PC memory card to the waveform memory of MU368030A Universal Modulation Unit.

The software is installed in the expansion unit for provision. Also, PC memory card is supplied for backup.

The software can be instantly switched over by selecting the installed software.

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Downlink MX368041B ↔ MX368041A

CDMA Scrambling Code Edit

Scrambling Code Generator 1
 Scrambling Code Number : [0 0000]
 Scrambling Code Period : 0 9600
 Clock Shift : 0 0000 Q Phase Offset : 2 0000
 Chip Offset : [0 0000]

Scrambling Code Generator 2
 Scrambling Code Number : [0 0000]
 Scrambling Code Period : 0 9600
 Clock Shift : 0 0000 Q Phase Offset : 2 0000
 Chip Offset : [0 0000]

Scrambling Code Generator 3
 Scrambling Code Number : [0 0000]
 Scrambling Code Period : 0 9600
 Clock Shift : 0 0000 Q Phase Offset : 2 0000
 Chip Offset : [0 0000]

I/Q Phase : [0RAD]
 Trigger Delay : [0.0Chip]

CDMA Scrambling Code Edit

Scrambling Code Generator 1
 Scrambling Code Number : [0 0000]
 Scrambling Code Period : [0 9600]
 Clock Shift : [0 0000] Q Phase Offset : [2 0000]
 Chip Offset : [0 0000]

Scrambling Code Generator 2
 Scrambling Code Number : [0 0000]
 Scrambling Code Period : [0 9600]
 Clock Shift : [0 0000] Q Phase Offset : [2 0000]
 Chip Offset : [0 0000]

Scrambling Code Generator 3
 Scrambling Code Number : [0 0000]
 Scrambling Code Period : [0 9600]
 Clock Shift : [0 0000] Q Phase Offset : [2 0000]
 Chip Offset : [0 0000]

I/Q Phase : [π/4 RAD]
 Trigger Delay : [0.0Chip]

- Unable to set
 - Scrambling Code Period
 - Clock Shift
 - Q Phase Offset
- I/Q Phase: default setting [0RAD]

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Uplink MX368041B ↔ MX368041A

CDMA Scrambling Code Edit

Scrambling Code Generator 1
 Scrambling Type : Long Scrambling Code Period : 000 9600
 C long,1,n x_n : [00 0000] C long,1,n y : 1FF FFFF
 C long,2,n x_n : 0 12 0040 C long,2,n y : 018 00FF
 Chip Offset : [0 0000]

Scrambling Code Generator 2
 Scrambling Type : Long Scrambling Code Period : 000 9600
 C long,1,n x_n : [00 0000] C long,1,n y : 1FF FFFF
 C long,2,n x_n : 0 12 0040 C long,2,n y : 018 00FF
 Chip Offset : [0 0000]

Scrambling Code Generator 3
 Scrambling Type : [Long] Scrambling Code Period : 000 9600
 C long,1,n x_n : [00 0000] C long,1,n y : 1FF FFFF
 C long,2,n x_n : 0 12 0040 C long,2,n y : 018 00FF
 Chip Offset : [0 0000] Short Code Number : ———

I/Q Phase : [0RAD]
 Trigger Delay : [0.0Chip]
 Noise Bandwidth : [Chiprate X2]

CDMA Scrambling Code Edit

Scrambling Code Generator 1
 Scrambling Type : Long Scrambling Code Period : [000 9600]
 C long,1,n x_n : [100 0000] C long,1,n y : [1FF FFFF]
 C long,2,n x_n : [012 0040] C long,2,n y : [018 00FF]
 Chip Offset : [0 0000]
 QPSK/HPSK Select : [HPSK] HPSK Decimation Factor : [2]

Scrambling Code Generator 2
 Scrambling Type : Long Scrambling Code Period : [000 9600]
 C long,1,n x_n : [100 0000] C long,1,n y : [1FF FFFF]
 C long,2,n x_n : [012 0040] C long,2,n y : [018 00FF]
 Chip Offset : [0 0000]
 QPSK/HPSK Select : [HPSK] HPSK Decimation Factor : [2]

Scrambling Code Generator 3
 Scrambling Type : [Long] Scrambling Code Period : [000 9600]
 C long,1,n x_n : [100 0000] C long,1,n y : [1FF FFFF]
 C long,2,n x_n : [012 0040] C long,2,n y : [018 00FF]
 Chip Offset : [0 0000] Short Code Number : ———
 QPSK/HPSK Select : [HPSK] HPSK Decimation Factor : [2]

I/Q Phase : [π/4 RAD]
 Trigger Delay : [0.0Chip]
 Noise Bandwidth : [Chiprate X2]

- Unable to set
 - Scrambling Code Period
 - C long,1,n x_n x_n(24)=1
 - C long,1,n y y(0)=y(1)...=y(23)=y(24)=1
 - C long,2,n x_n Calculates C long,1,n Xn value automatically
 - C long,2,n y Calculates C long,1,n y value automatically
- Menu disappearance
 - QPSK/HPSK Select
 - HPSK Decimation Factor
 - [F1~3] Default Code 1~3
- I/Q Phase: default setting [0RAD]

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Specifications are subject to change without notice.

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