

TECHNICAL NOTE

- Random Error Function -

MP1590A

Network Performance Tester

ANRITSU CORPORATION

Copyright © 2004 by ANRITSU CORPORATION

The contents of this manual shall not be disclosed in any way or reproduced in any media without the express written permission of Anritsu Corporation

1: Necessity of Random Error Insertion

Generally, error correction codes classified as block codes are used for correcting random errors (errors that can occur on actually circuits). The ITU-T G.709 Optical Transport Network (OTN) has adopted Reed-Solomon codes (RS (255, 239)) as a technique for Forward Error Correction (FEC). Because Reed-Solomon codes are block codes, pseudo-random errors must be generated to evaluate the performance of an FEC decoder, for example, by comparing the performance of FEC as against the theoretical value curve. The theoretical value curve shown in Figure 1 has been calculated under the condition of generating random errors.

When inserting errors at a specific error rate, however, the performance of FEC cannot be evaluated properly if errors are generated at even intervals. To correctly evaluate the performance of FEC, errors must be generated at random to create various states where errors can be corrected or where errors cannot be corrected.

For random errors, the error rate is satisfied over a long period of time. Within a short period of time, the error rate fluctuates as against the set value. This is close to the actually circuits and is therefore suitable for evaluating the performance of FEC.





When the error rate is 1/n



Figure 2 Error insertion

2: Principle of Random Error Insertion

2-1 Binominal Distribution and Poisson Distribution

When two types of events (A and B) can occur, trials are repeated and each trial is independent (the results of former trials do not affect the result of the current trial), and probability p of obtaining result A is constant throughout the trials, these trials are called Bernoulli Trials. For example, using the pieces in the game Othello, consider the probability of the white side of the pieces coming up when they are thrown repeatedly. If none of the pieces have any deformation and are coated with white and black paints of equal weight, this results in Bernoulli Trials with the probability of obtaining the white side p = 1/2.

The probability of obtaining result A k times when trials are repeated n times can be expressed by formula (1) shown below. This probability distribution is called a binominal distribution. In formula (1), μ indicates the mean value and 2 indicates the variance.

Explaining formula (1) by taking error generation as an example, it indicates the probability distribution for k bit errors in n bits. However, the calculation of formula (1) becomes more and more complicated as the value of n increases. It is actually difficult to directly calculate this formula with values assigned.

Now, assume = np, where is a fixed value. This indicates that p decreases when n increases and p increases when n decreases. Then, assume an extreme binominal distribution in which n = and p = 0. This leads to the following formula:

The probability distribution expressed in formula (2) is called a Poisson distribution. Since is fixed, this distribution is indicated as a distribution by variable k. To relate the Poisson distribution to the binominal distribution, assign np to . Then, formula (3) is obtained.

$$f(k;np) = e^{-(np)} \frac{(np)^{k}}{k!} ---(3)$$





Figure 3 Poisson distribution

Figure 3 shows the Poisson distribution as a graph. This graph indicates the probability distribution for k occurrences of an event as against mean value np.

For example, consider the probability of a baseball player with a batting average of .300 scoring k hits within 10 at-bats. In this case, $\lambda = np = 10 \times 0.3 = 3$; so the player scores three hits on average. Actually, however, the player might score eight hits within 10 at-bats or only one hit within another 10 at-bats. Under these conditions, the probability that this player scores five hits within 10 at-bats is calculated using formula (3), as follows:

That is, the player scores five hits within 10 at-bats with a probability of approximately 10%.

This result is indicated by the probability distribution shown as the Poisson (3,x) graph in Figure 2. In the Poisson (3,x) graph, the peak comes at k = 3 because this player scores three hits on average. The probability that the player scores three hits within 10 at-bats is 22.4%. The probability goes down both when the number of hits increases and when it decreases.

2-2 Poisson Distribution and Error Rate

The following describes random error generation in relation to Poisson distribution.

In this example, p indicates an element error rate (error rate). np indicates the average error bit count μ when the frame length is n bits. For example, when p is fixed, $n = \mu / p$.

Figure 4 indicates the result obtained by applying this error rate example to Figure 3. The frame length n increases as 1×10^3 , 2×10^3 , 3×10^3 ,bits when the error rate p is 1×10^{-3} and fixed, and the average error bit count increases as 1, 2, 3, and so on.



Probability distribution of variable "k" when "np" is fixed (Poisson distribution)

Figure 4 Distribution when $p = 1x10^{-3}$

In the figure 4, k indicates the error bit count. When the average error bit count np is 1 (Poisson (1,X)), the probability that one bit error (k = 1) is the highest. k = 2 indicates the probability that two out of $1x10^3$ bits error occur, and this probability is lower. In the same way, when np = 5 (Poisson (5,x)), the probability that five bits error occur is the highest. The probabilities that more than five or less than five out of $5x10^3$ bits error occur are lower.

Random errors can be generated based on this probability distribution.

The following describes the relationship between error rates and Poisson distribution from another point of view. Figure 5 shows the Poisson distribution when error bit count k is fixed and average error bit count np is changed.



Probability distribution of variable "np" when "k" is fixed (Poisson distribution)

Figure 5 Poisson distribution considered by assuming np as a variable

Figure 5 (Poisson (x, 1)) shows the probability distribution for one bit error (k = 1) for np. When the

error rate is fixed at $p = 1x10^{-3}$ (on average, one out of 1000 bits), the peak comes at n = 1000 (bits). When np = 2, the probability is that two out of 2000 bits error occur. This probability is lower than that for np = 1. In the same way, Poisson (x,3) indicates the probability distribution for three (k = 3) error bits. When the error rate is fixed at $p = 1x10^{-3}$, the peak comes at n = 3000 (bits).

Figure 6 shows an actual image of random error insertion.



When data is separated into n-bit blocks, error bit count "k" inserted into each block has a Poisson distribution.

Figure 6 Image of random error insertion

When data is separated into n-bit blocks, the error bit count k inserted into each block has a Poisson distribution. The value of the average error bit count np determines which of the graphs shown in Figure 4 indicates the distribution of error bit count k.

2-3 Specification for Random Error Insertion for the MP1590A

The random error insertion function of the MP1590A has been designed taking into consideration the randomization quality (each event occurs independently unaffected by previous or later events) and uniformity (each event occurs at the same probability). The MP1590A has satisfied these conditions to enable random error insertion based on a Poisson distribution.

The MP1590A can insert random errors using one of the Bitall error insertion timings. The error rate can be set within the range of 0.1×10^{-10} to 1.0×10^{-2} . Random errors can be inserted into all areas of the OTU1 and OTU2 frames.

(When fixed rate 1.0×10^{-3} or 1.0×10^{-2} is set for the error rate, random error insertion is automatically selected.)

3: Example of a Measurement System Using the MP1590A

3-1 FEC Tolerance Test

• FEC Decoder testing



Figure 7 FEC tolerance test

In an FEC tolerance test using a former SDH/SONET measuring instrument such as the MP1570A, an FEC converter and an optical attenuator must be connected between the DUT and the measuring instrument. In this case, the measurement system contains many devices and requires a large installation space. In addition, the test efficiency is reduced because several devices must be controlled simultaneously.

The MP1590A, however, has built-in FEC, random error insertion, and optical attenuation functions. Accordingly, a measurement system can be constructed by using the MP1590A and DUT only. In addition, since the random error insertion function of the MP1590A creates a condition close to that of physical circuits, more accurate FEC tolerance tests can be performed.

3-2 Inserting Random Errors into OTU Frames that Support FEC Other Than RS (255, 239)

By using the MP1590A in through mode, random errors can be inserted into an OTU frame that is coded using FEC codes other than RS (255, 239) (the customer's own FEC codes that use the parity check area of the OTU frame).

As shown in Figure 8, the MP1590A and DUT are connected and the MP1590A is set to transparent through mode and FEC OFF. This enables the received OTU frame coded using the customer's own FEC codes to be output from MP1590A Transmitter.

The random error insertion function of the MP1590A then adds errors to the OTU frame. The DUT receives signals with random errors attached and performs error correction using the FEC codes. The FEC performance can thus be evaluated.



Figure 8 Evaluating FEC using through mode

(Blank Page)

ritsu

ANRITSU CORPORATION

1800 Onna, Atsugi-shi, Kanagawa, 243-8555 Japan Phone: +81-46-223-1111 Fax: +81-46-296-1264

U.S.A. ANRITSU COMPANY TX OFFICE SALES AND SERVICE

1155 East Collins Blvd., Richardson, TX 75081, U.S.A. Tol Free: 1-800-ANRITSU (267-4878) Phone: +1-972-644-1777 Fax: +1-972-644-3416

Canada

ANRITSU ELECTRONICS LTD.

```
700 Silver Seven Road, Suite 120, Kanata,
ON K2V 1C3, Canada
Phone: +1-613-591-2003
Fax: +1-613-591-1006
```

Brasil ANRITSU ELETRÔNICA LTDA.

Prace Amadeu Amaral, 27 - 1 andar 01327-010 - Panaiso, Seo Paulo, Brazil Phone: +55-11-3283-2511 Fax +55-11-3886940

U.K. ANRITSU LTD.

200 Capability Green, Luton, Bedfordshire LU1 SLU, U.K. Phone: +44-1582-433280 Fax: +44-1582-731303

 Germany ANRITSU GmbH

Grafenberger Allee 54-55, 40237 Düsseldorf, Germany Phone: +49-211-96855-0 Fax: +49-211-96855-55 France

ANRITSU S.A.

9, Avenue du Québec Z.A. de Courtaboeuf 91951 Les Ulla Cedex, France Phone: +33-1-60-92-15-50 Fax: +33-1-64-46-10-65

Italy

ANRITSU S.p.A. Via Elio Vittorini, 129, 00144 Roma EUR, Italy Phone: +39-08-509-9711 Fax: +39-06-502-2425

Sweden ANRITSU AB

Fagelviksvagen 9E S145 84 Stockholm, Sweden Phone: +45-853470700 Fax: +46-853470730

Singapore

ANRITSU PTE LTD. 10, Hoe Chiang Road #07-01/02, Keppel Towers, Singapore 059315 Phone: +65-6262-2400 Fax: +65-6262-2533

Specifications are subject to change without notice.

Hong Kong ANRITSU COMPANY LTD.

Suite 923, 9/F., Chinachem Golden Plaza, 77 Mody Road, Tsimshatsui East, Kowloon, Hong Kong, China Phone: +852-2301-4980 Fax: +852-2301-3545

P. R. China ANRITSU COMPANY LTD.

Beijing Representative Office Room 1515, Beijing Fortune Building, No. 5 North Road, the East 3rd Ring Road, Chao-Yang District Beijing 100004, P.R. China Phone: +86-10-6590-9230

Korea ANRITSU CORPORATION

8F Hyun Juk Bidg. 832-41, Yeoksam-dong, Kangnam-ku, Seoul, 135-080, Korea Phone: +82-2-553-6503 Fax: +82-2-553-6504

Australia

ANRITSU PTY LTD. Unit 3/170 Forster Road Mt. Waverley, Victoria, 3149, Australia

Phone: +61-3-9558-8177 Fax: +61-3-9558-8255 Taiwan

ANRITSU COMPANY INC.

7F, No. 316, Sec. 1, NeiHu Rd., Taipei, Taiwan Phone: +886-2-8751-1816 Fax: +886-2-8751-1817

031113



No. MP1590Arandom-E-E-1-(1.00) 公知 Printed in Japan 2004-3 AKD